

Fig.1

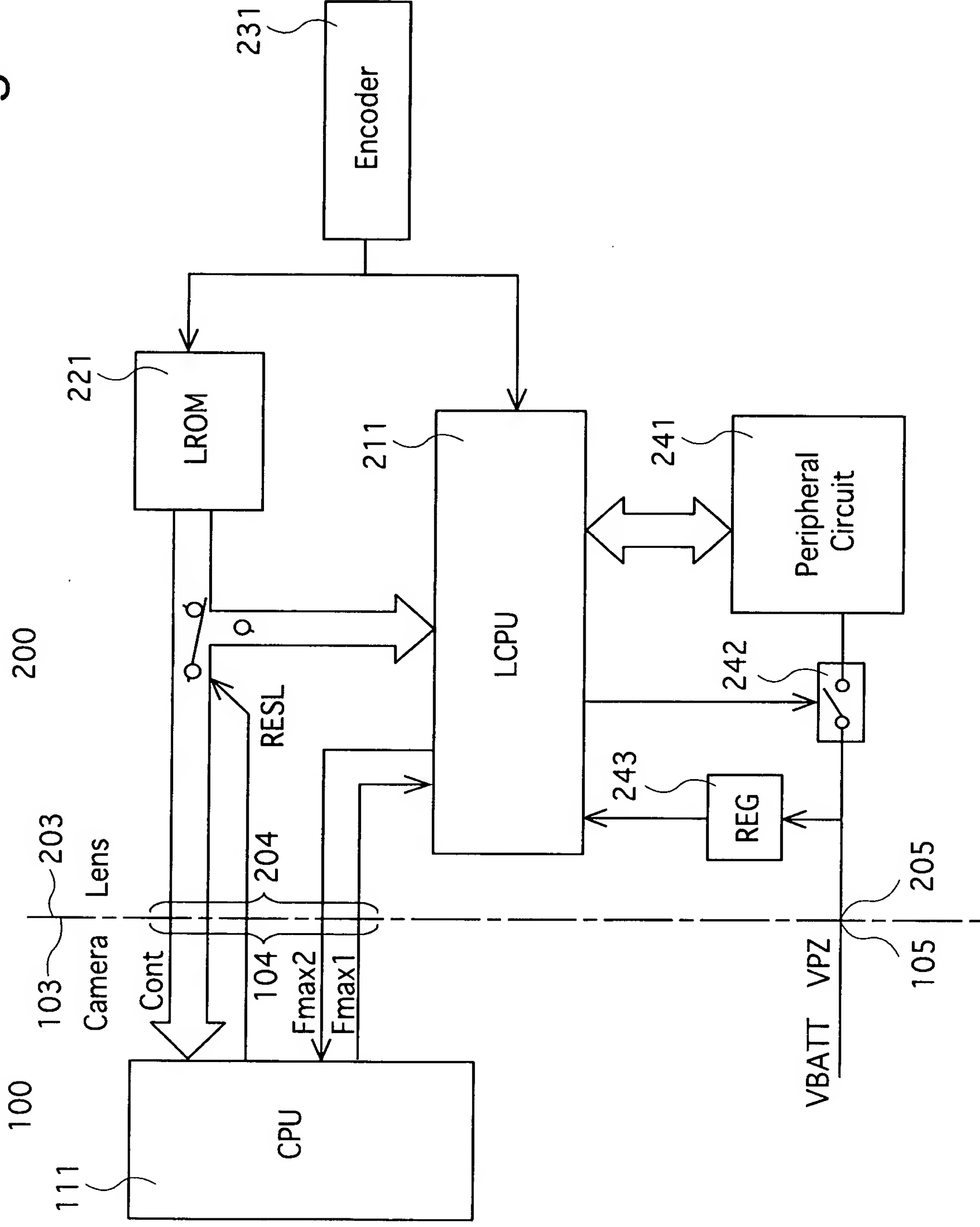


Fig.2

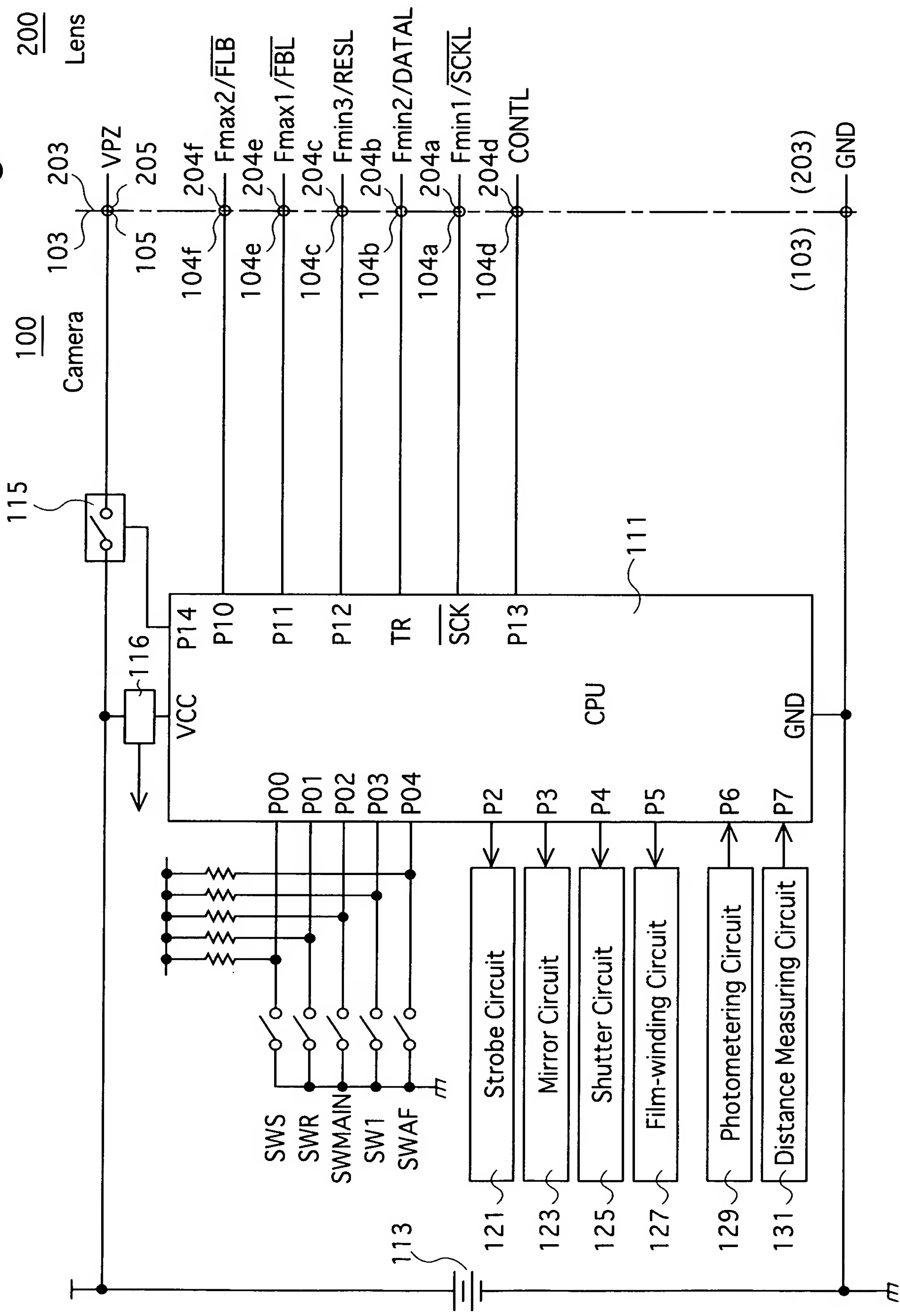


Fig.3

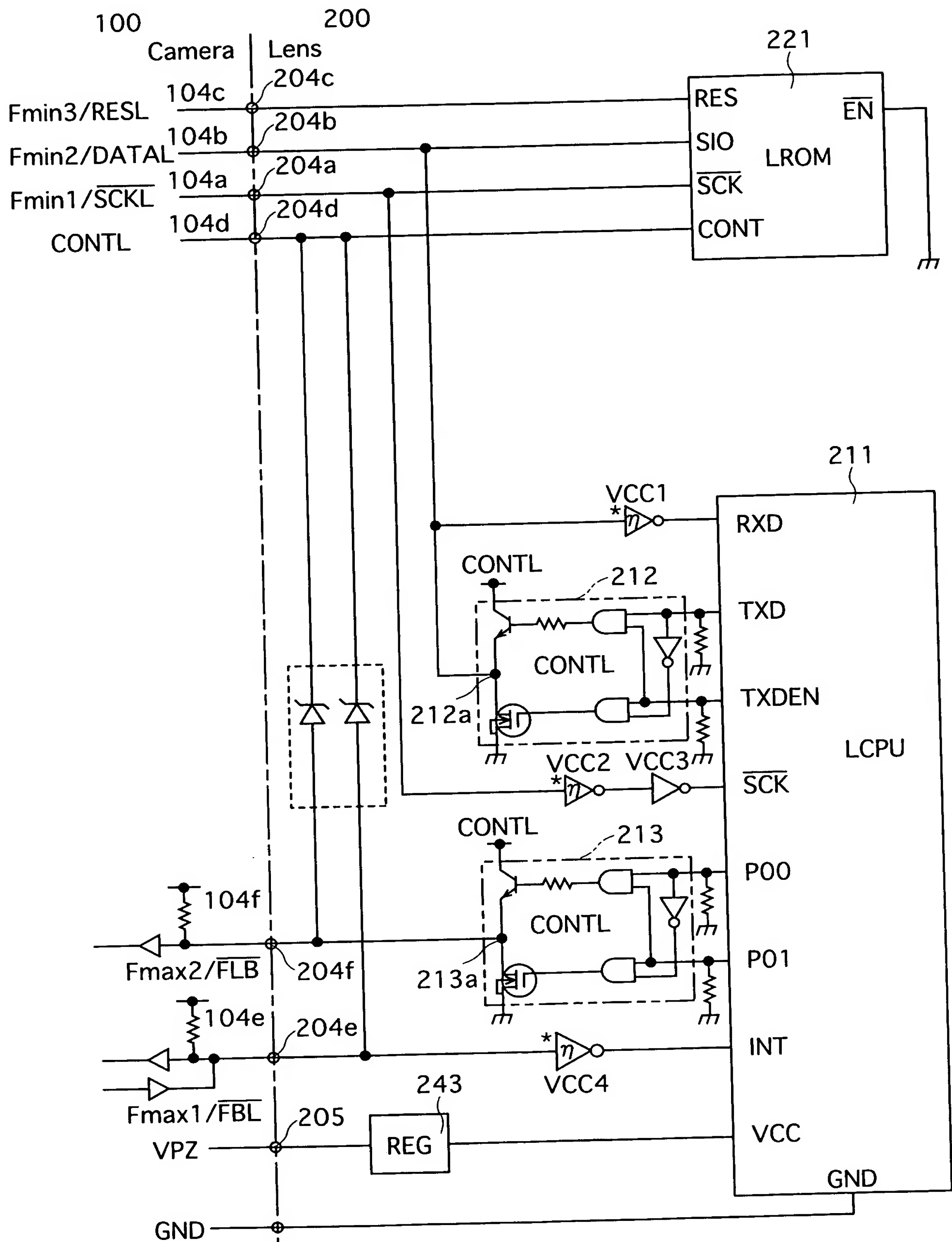


Fig.5

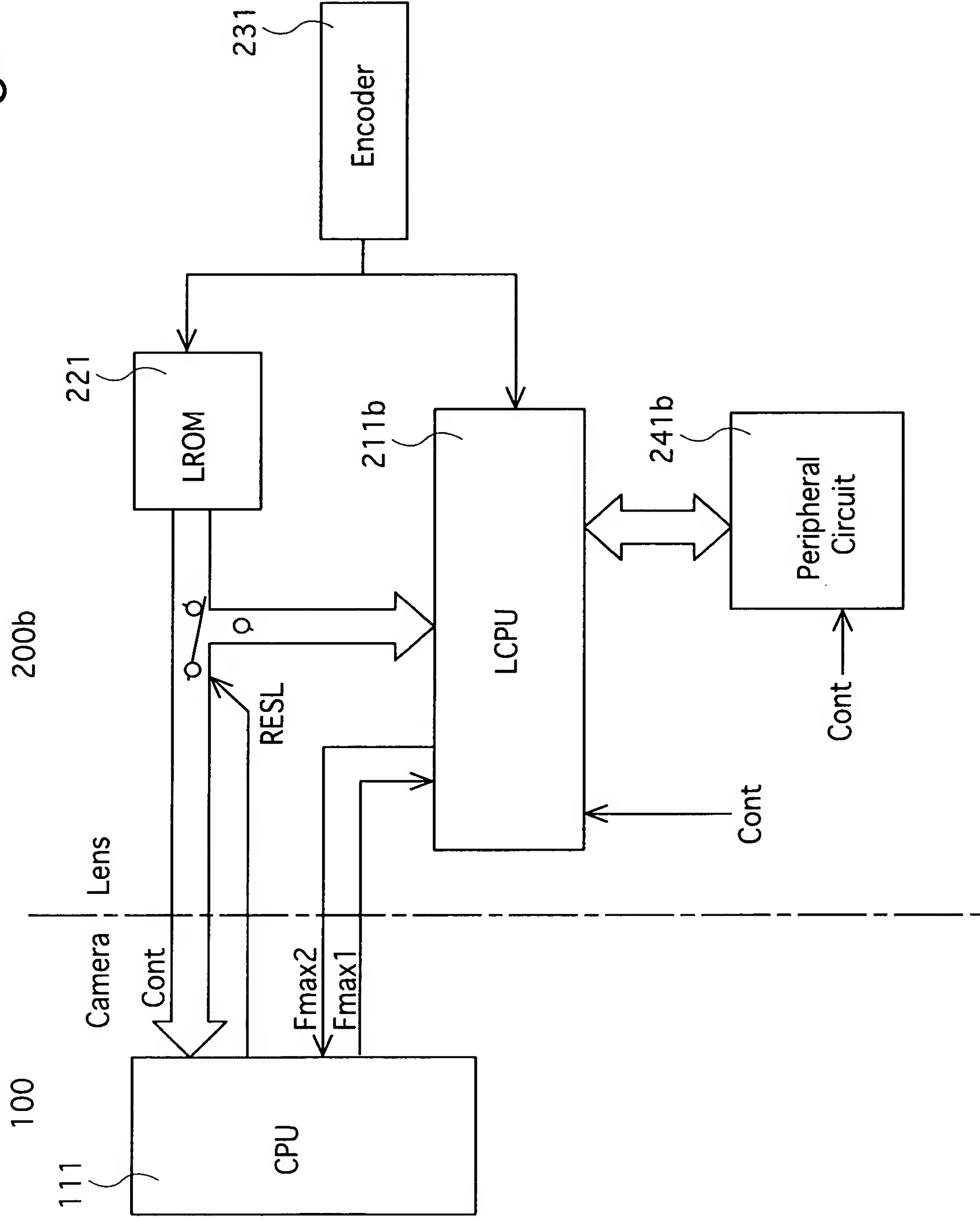


Fig. 6A

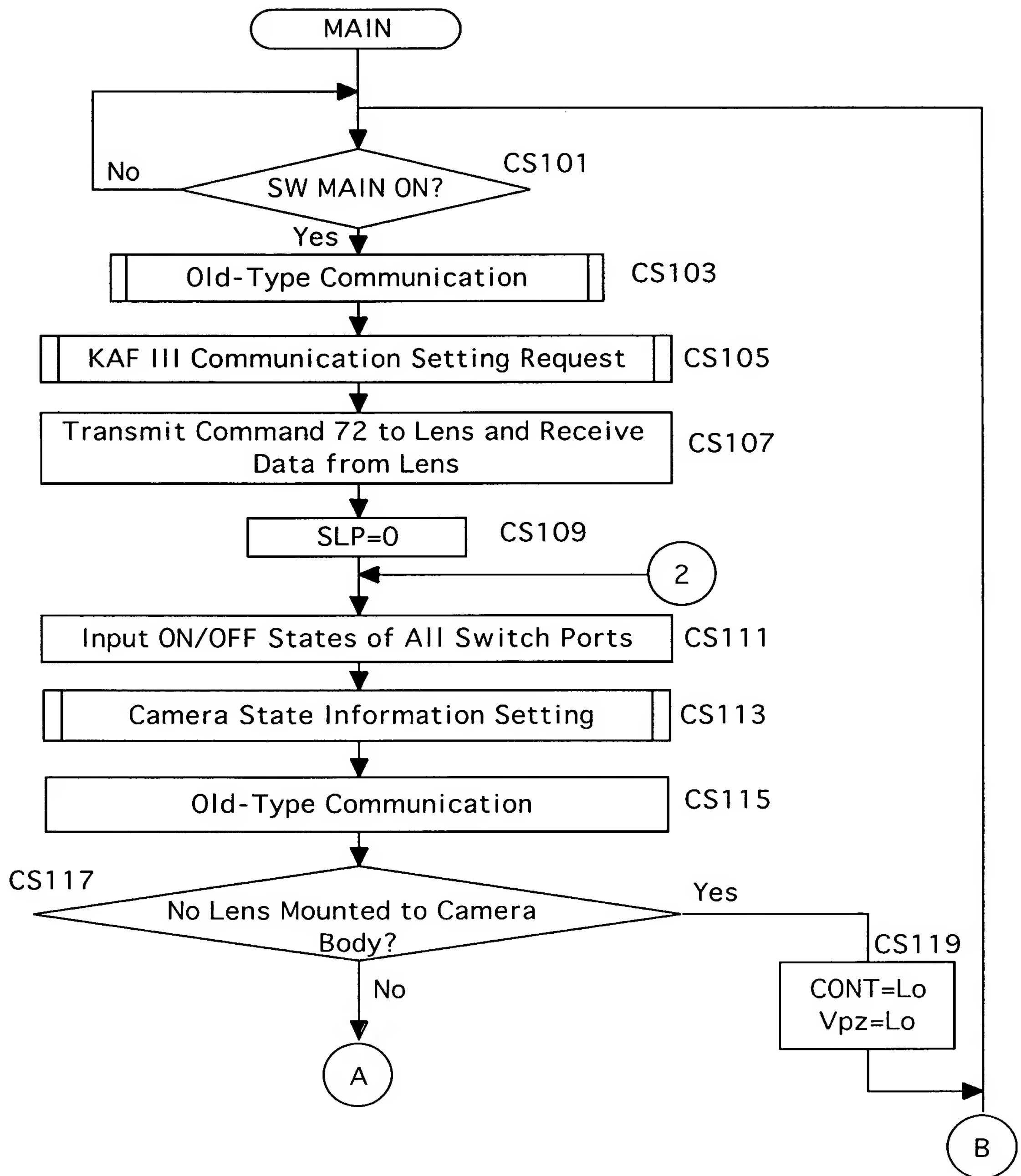


Fig. 6B

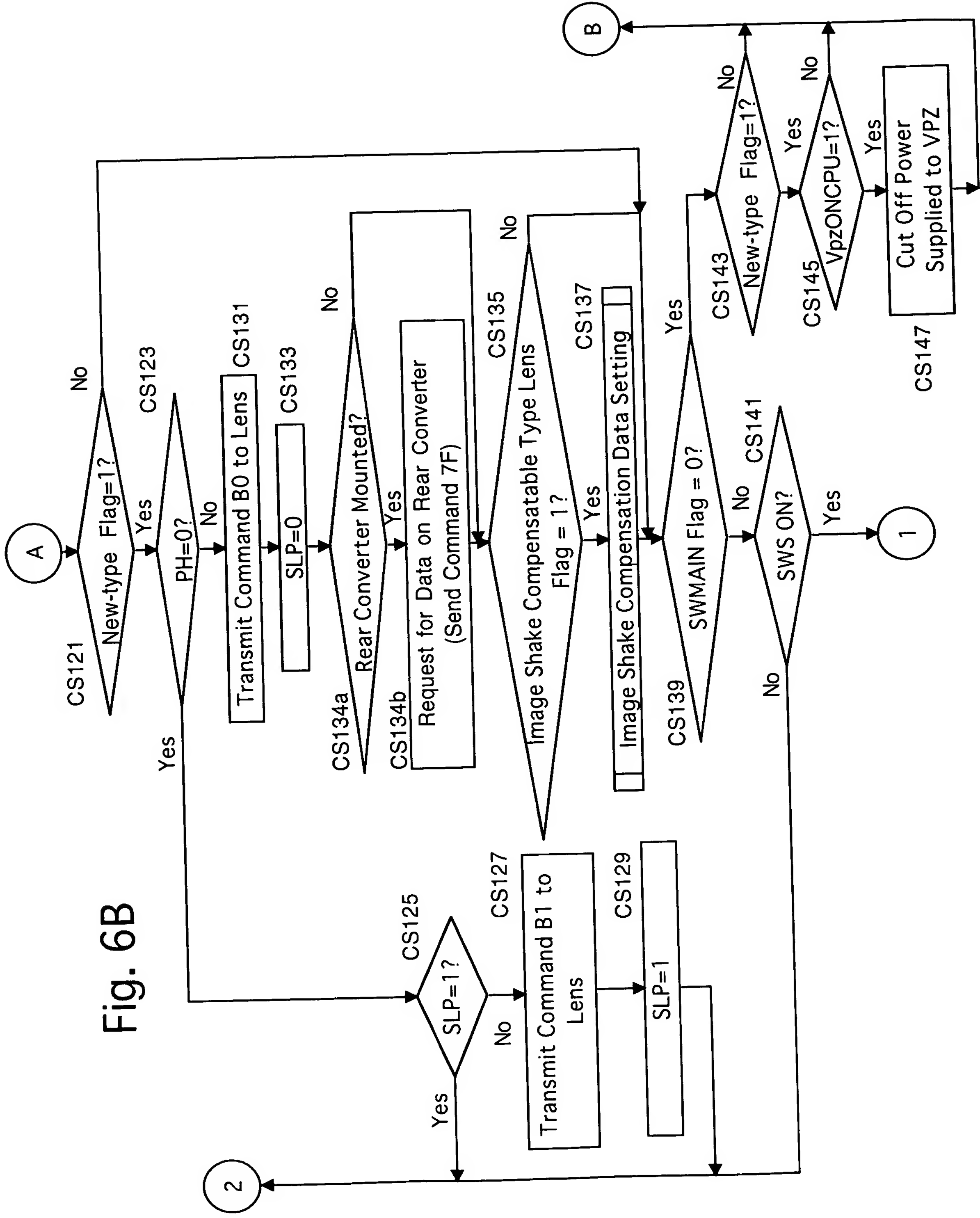


Fig. 7

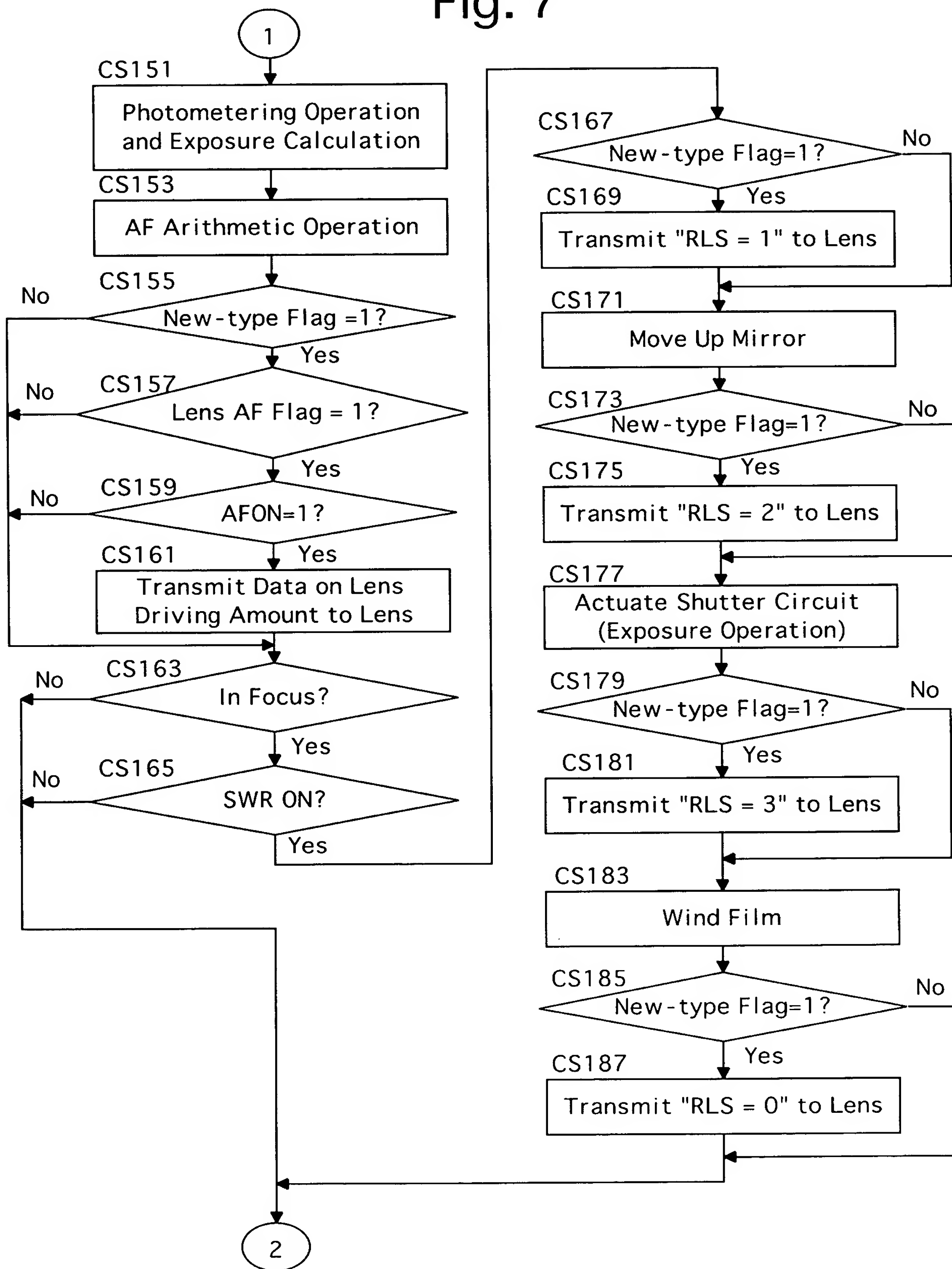


Fig. 8A

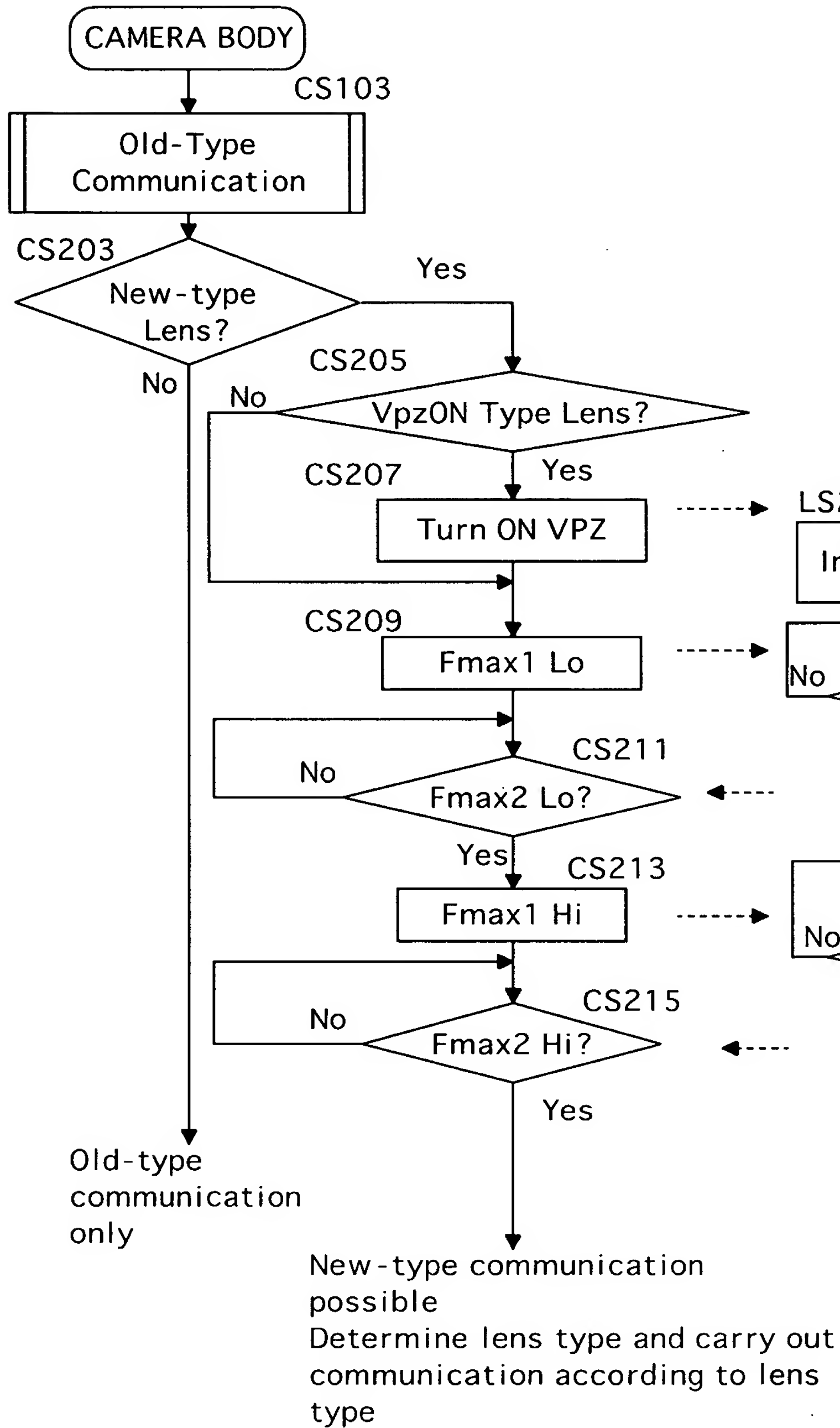


Fig.8B

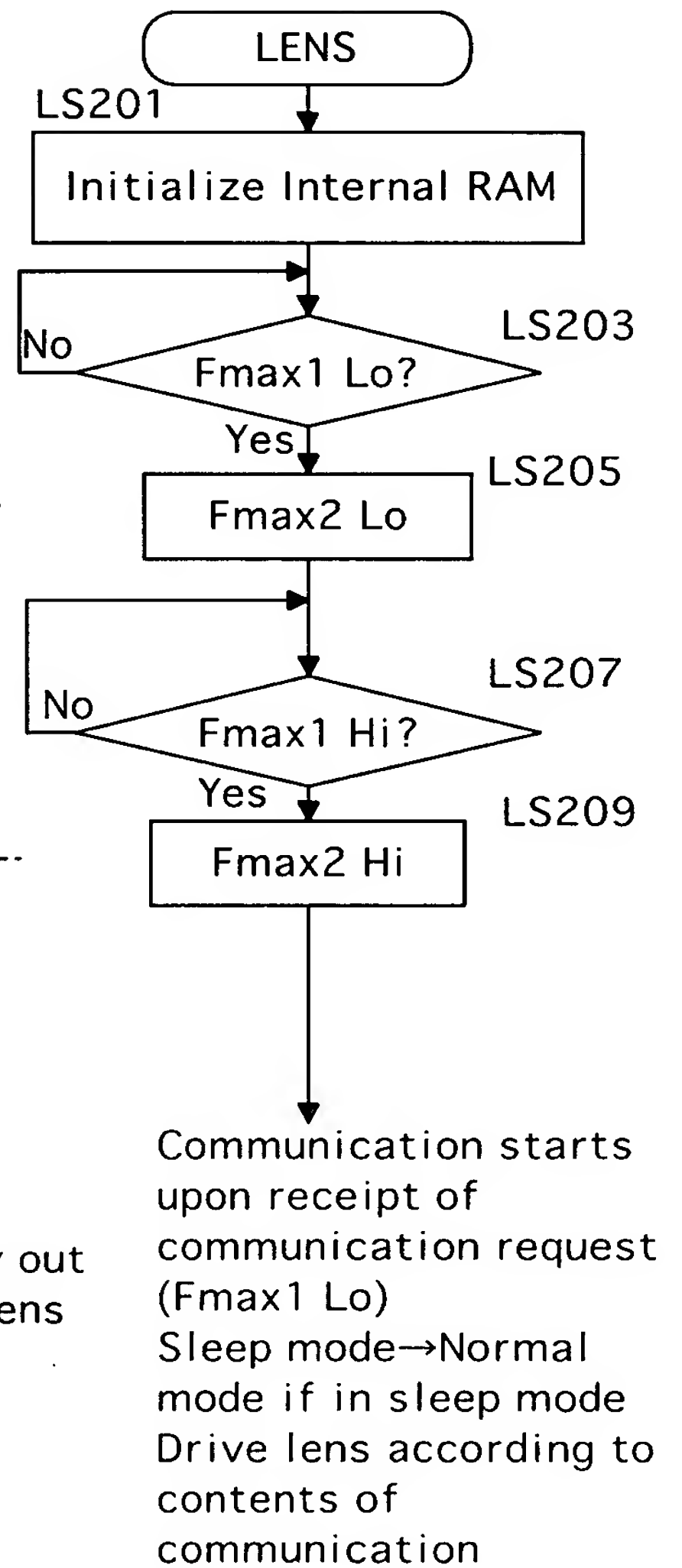
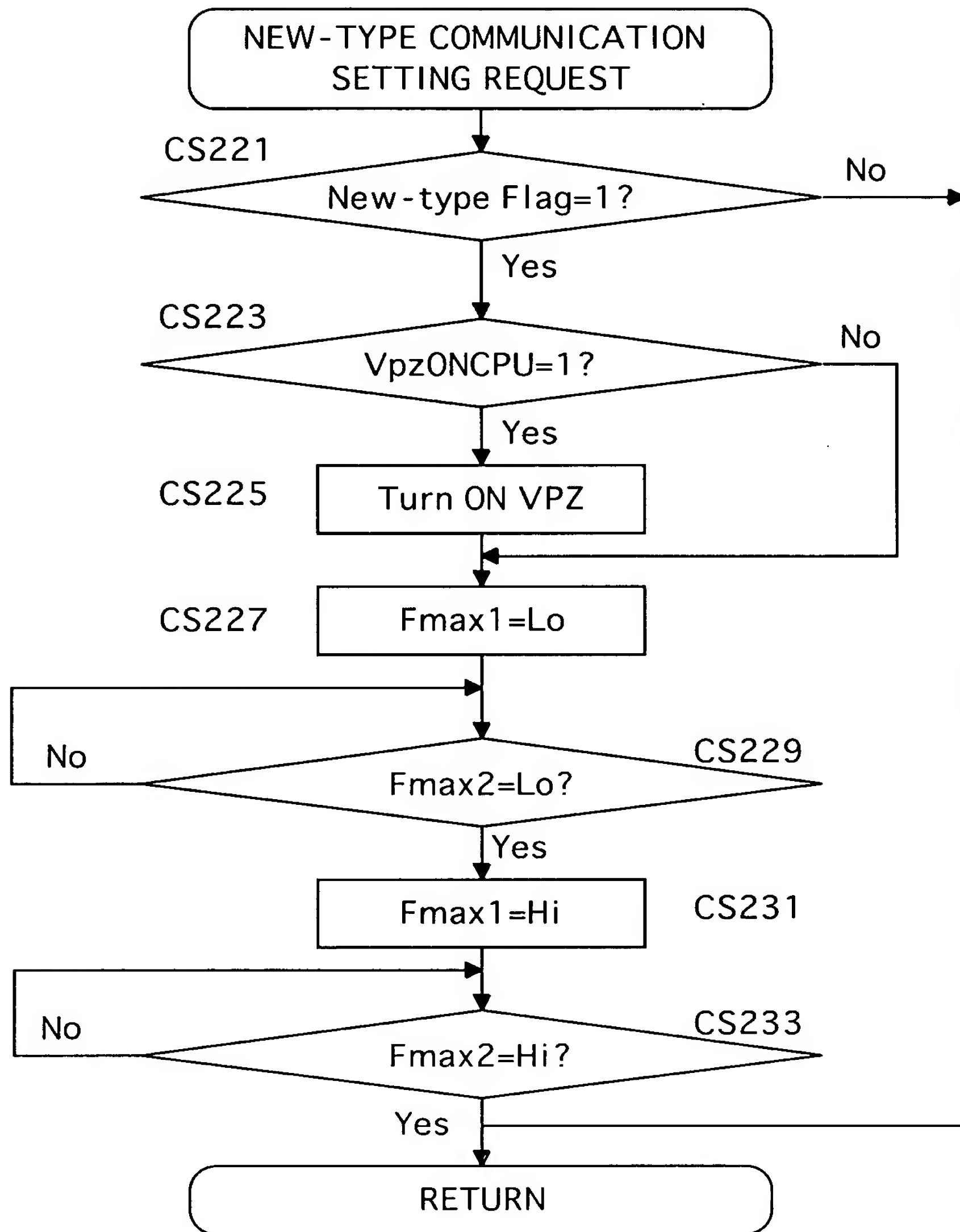
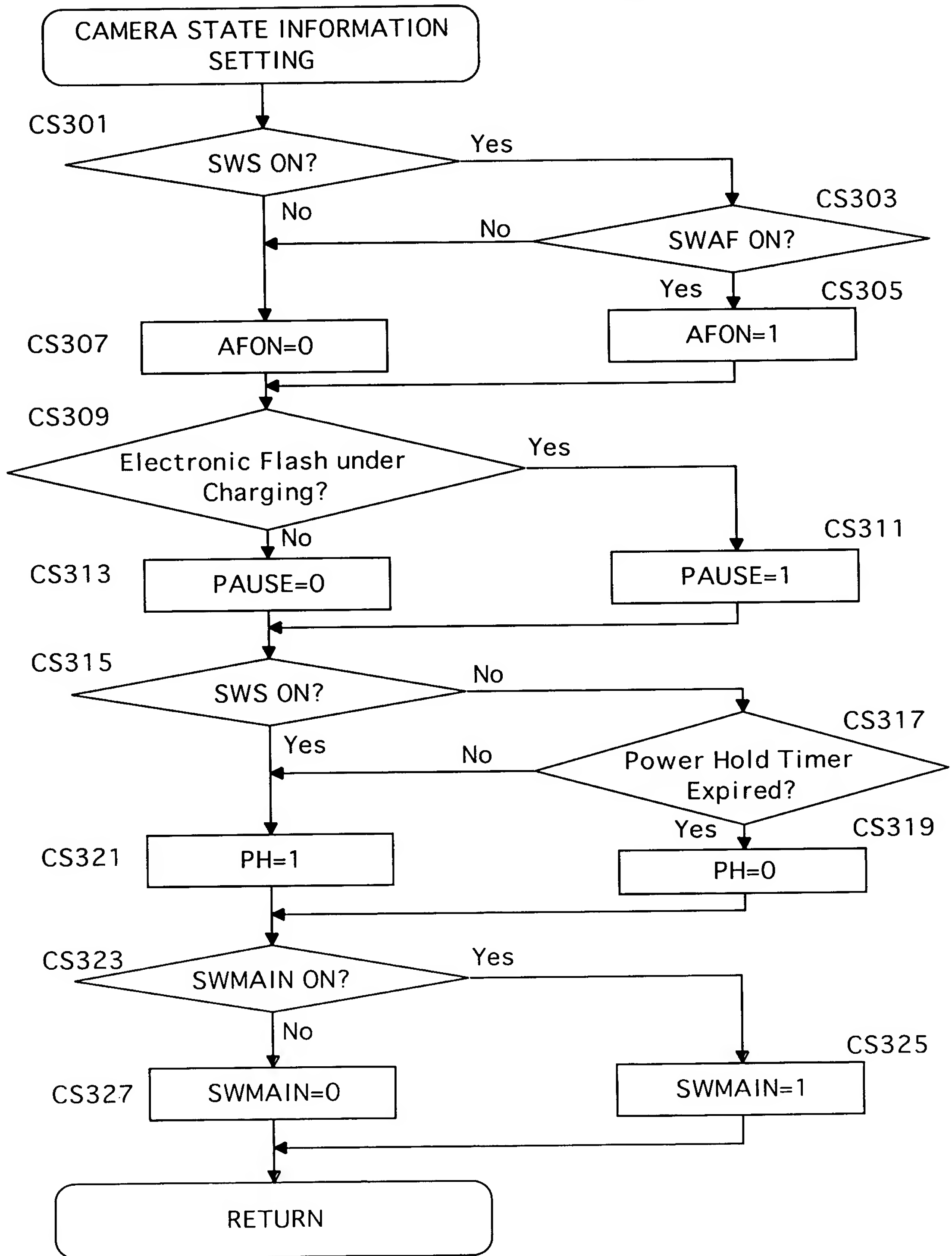


Fig.9



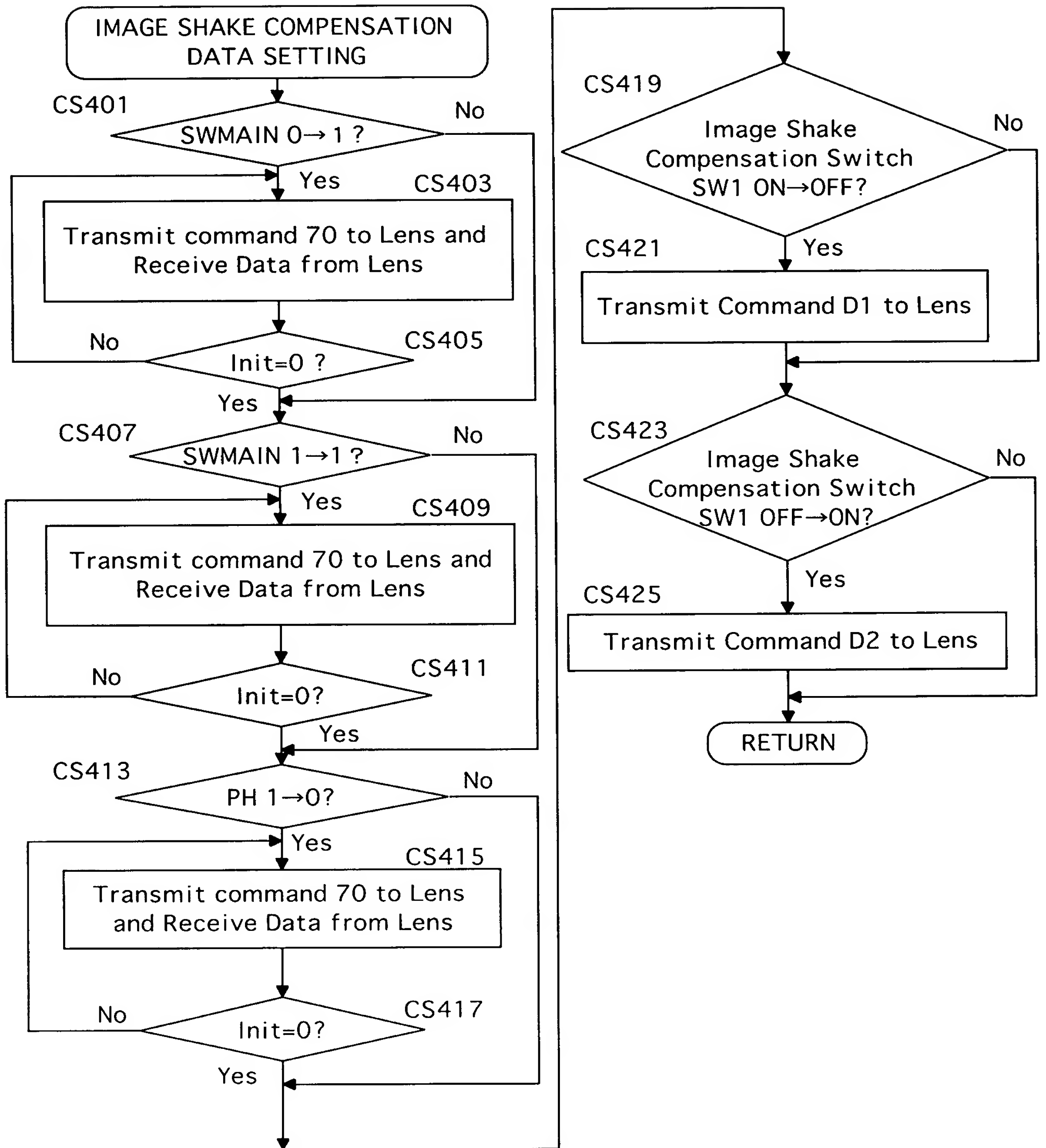
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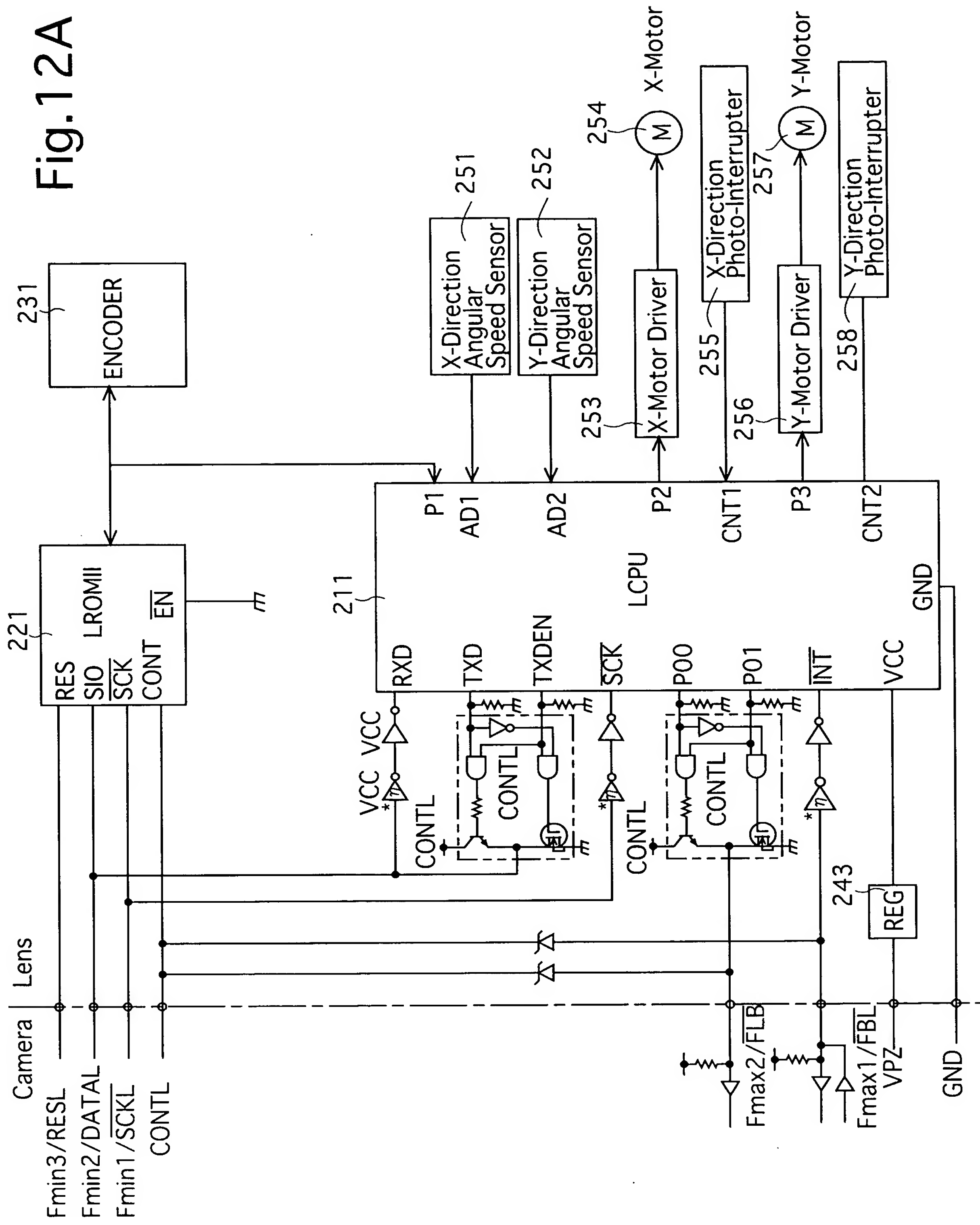
Fig.10



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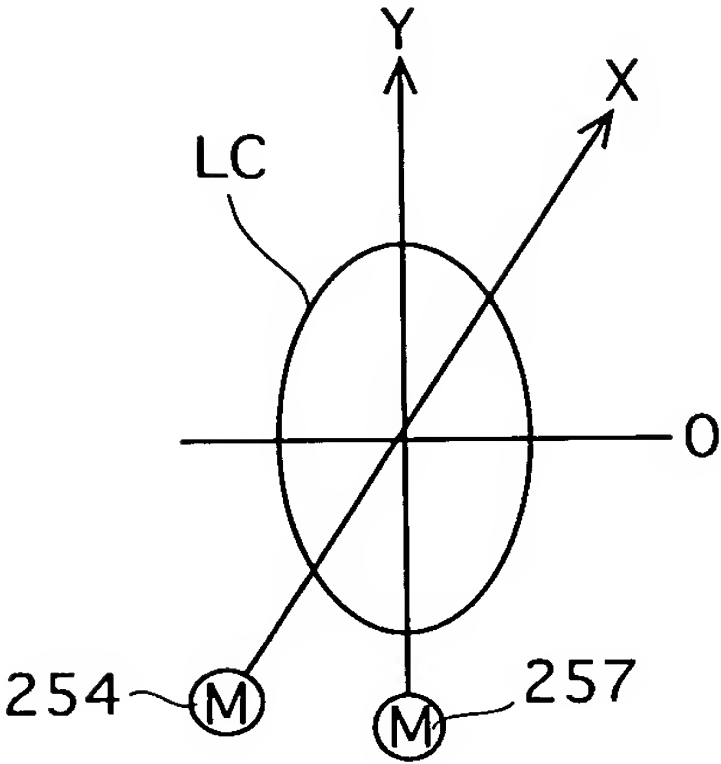
Fig.11





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Fig. 12B



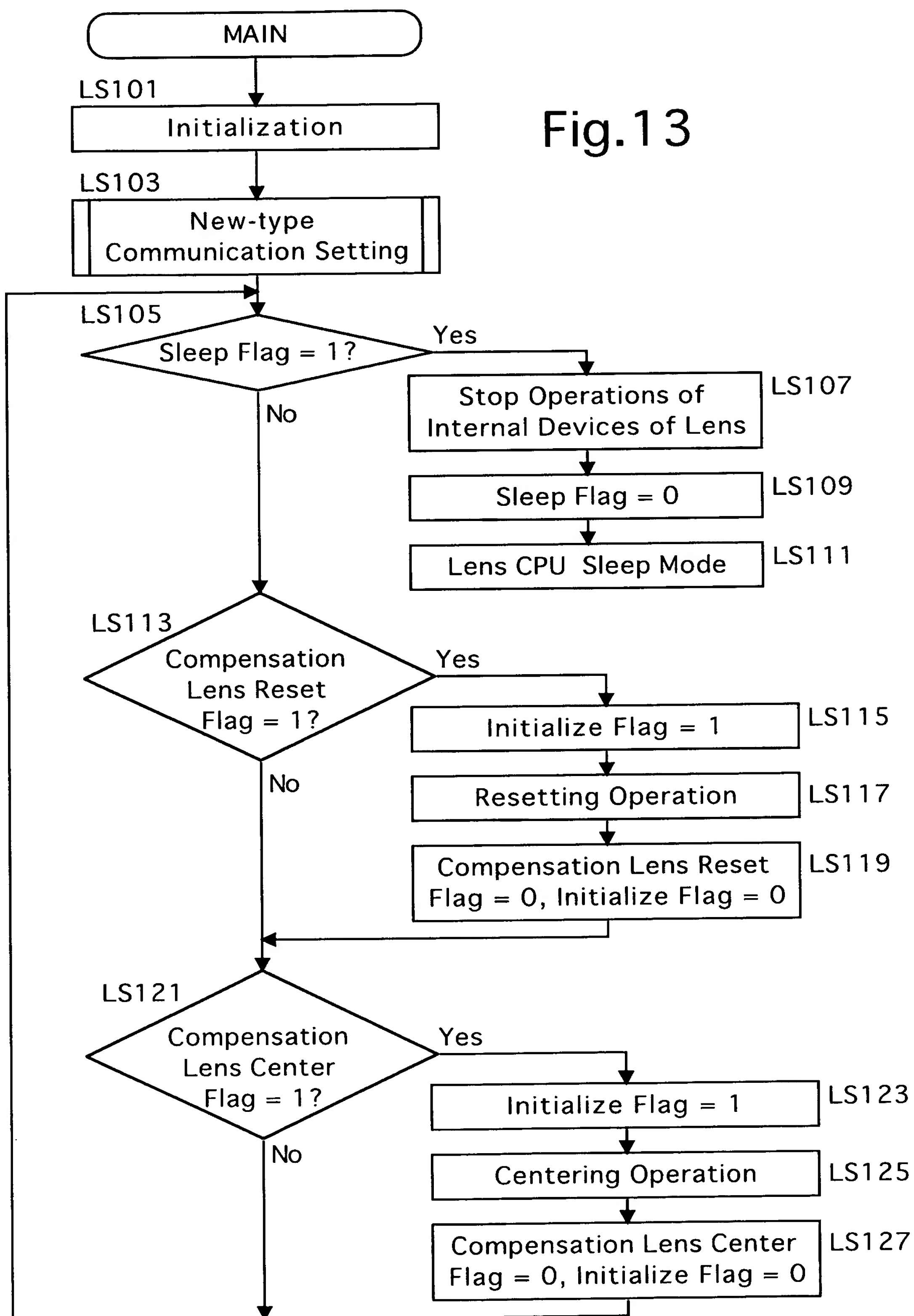
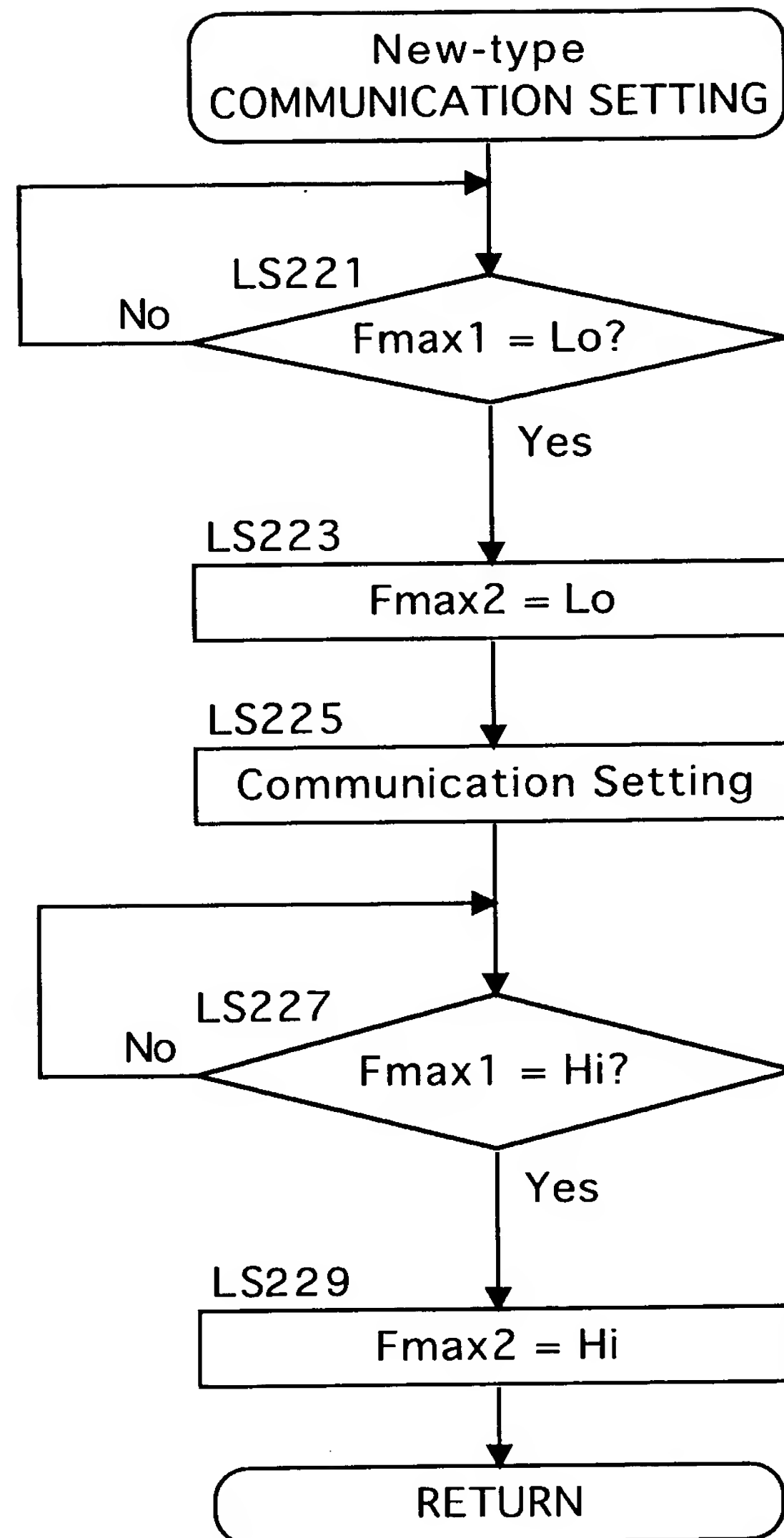
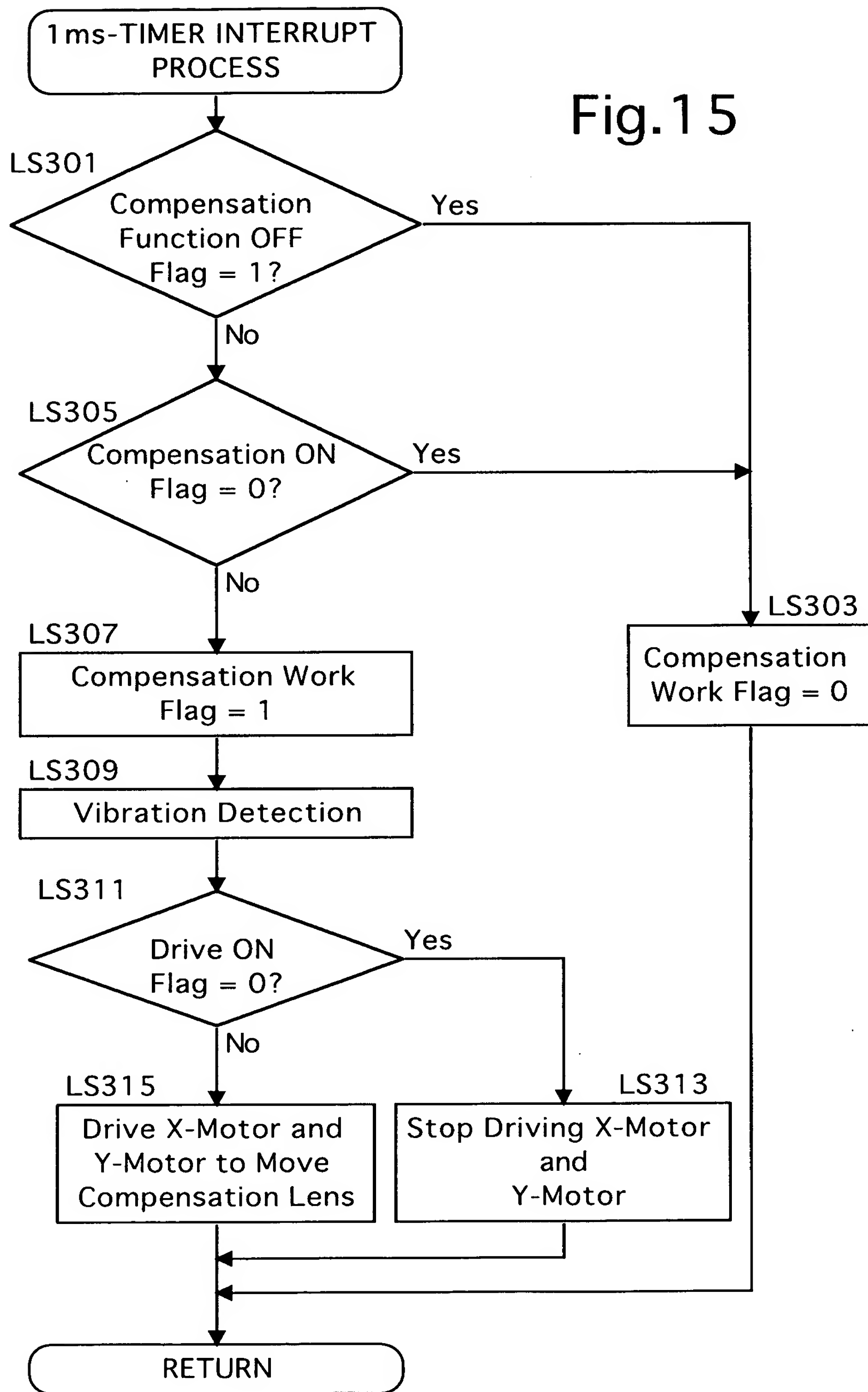
[illegible]

Fig.1 4



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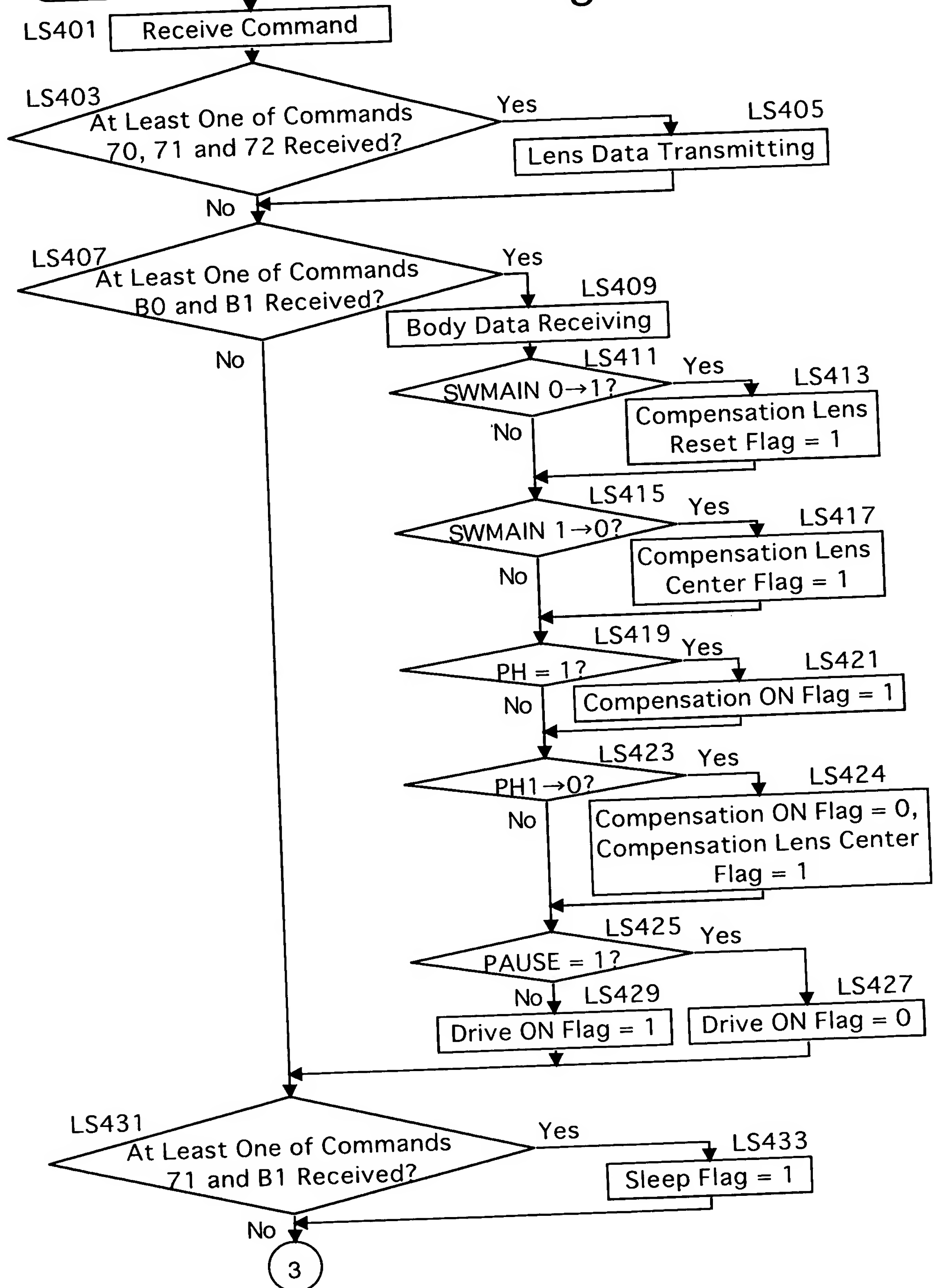
Fig.1 5



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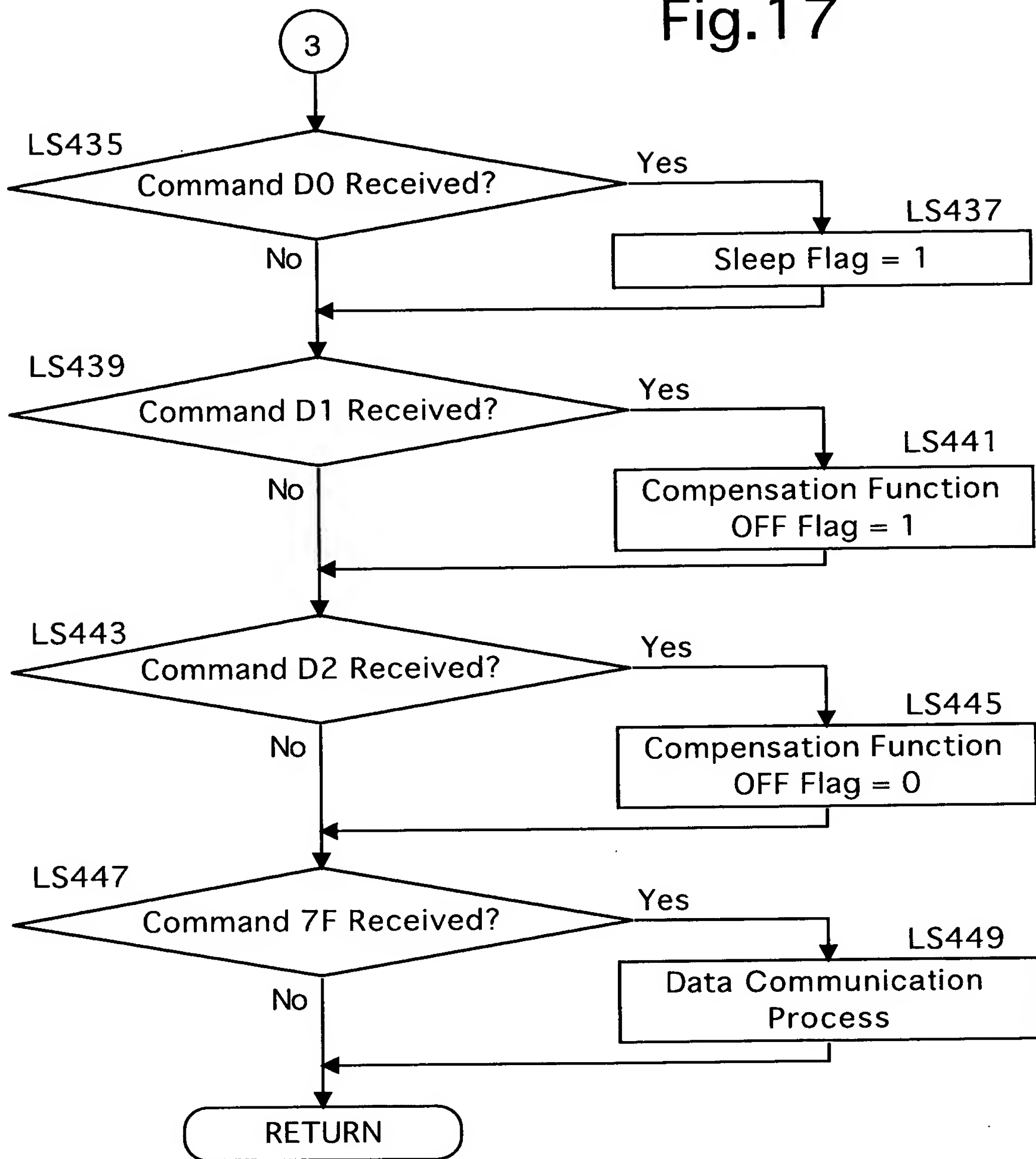
INVERSE-INT INTERRUPT PROCESS

Fig.16



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Fig.17



10083613-02702

Fig. 18

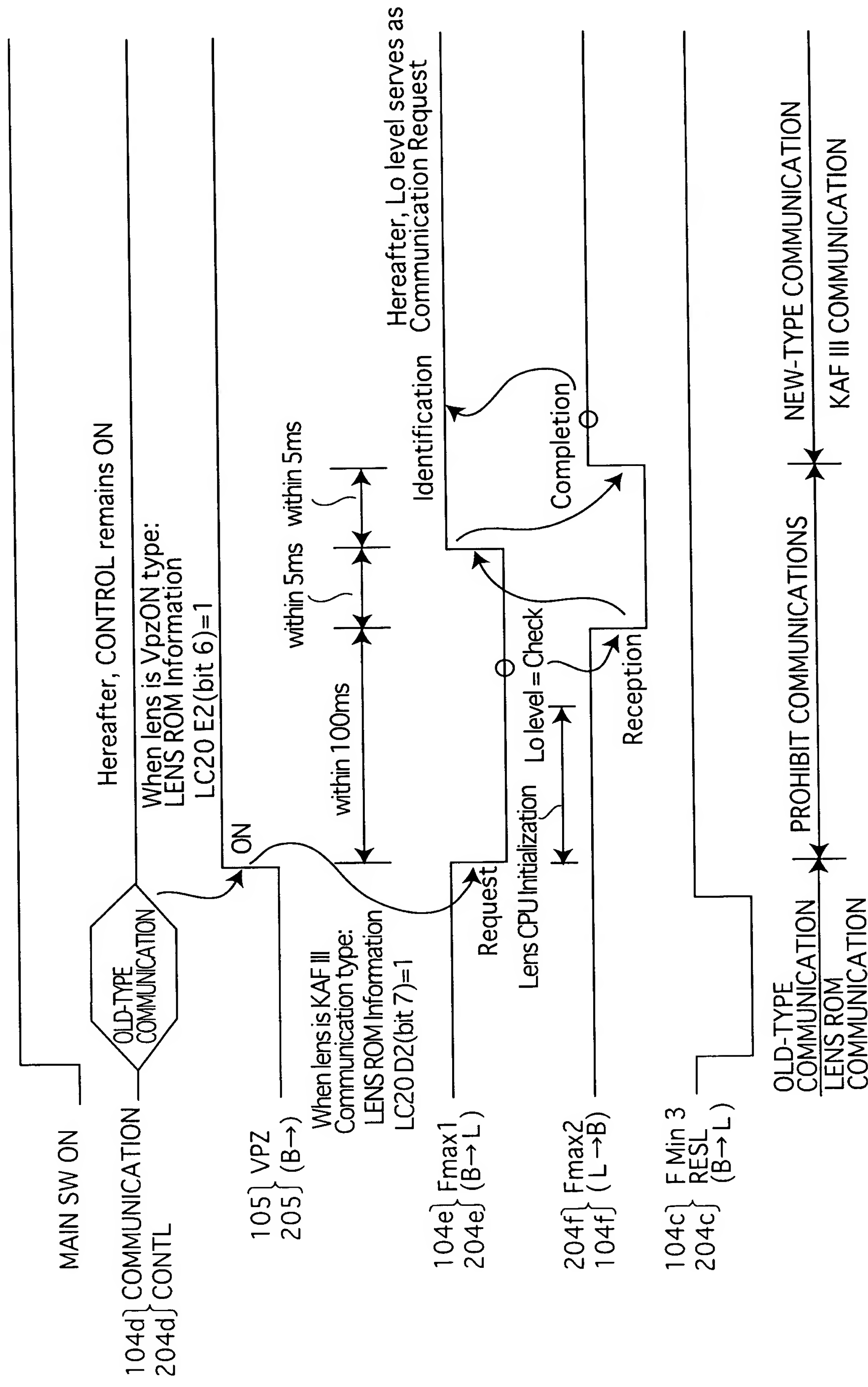


Fig. 19A

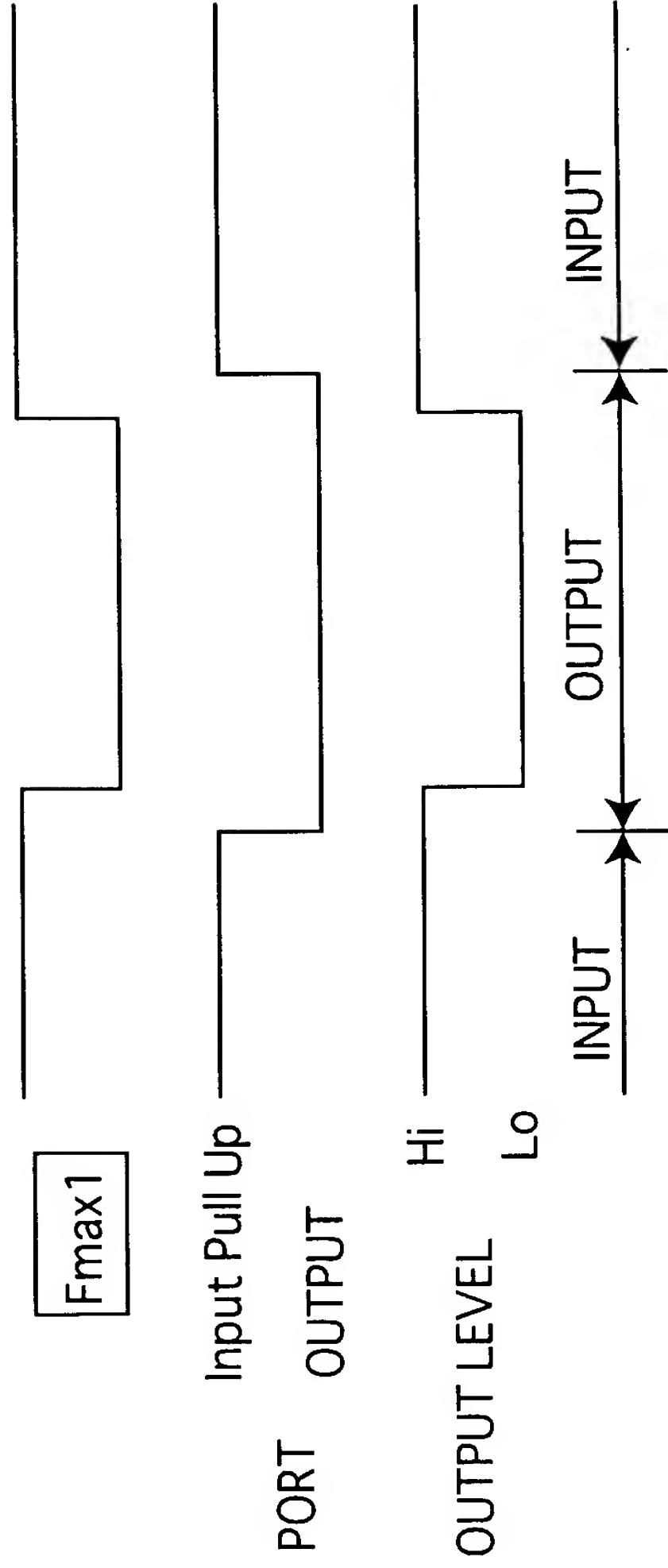


Fig. 19B

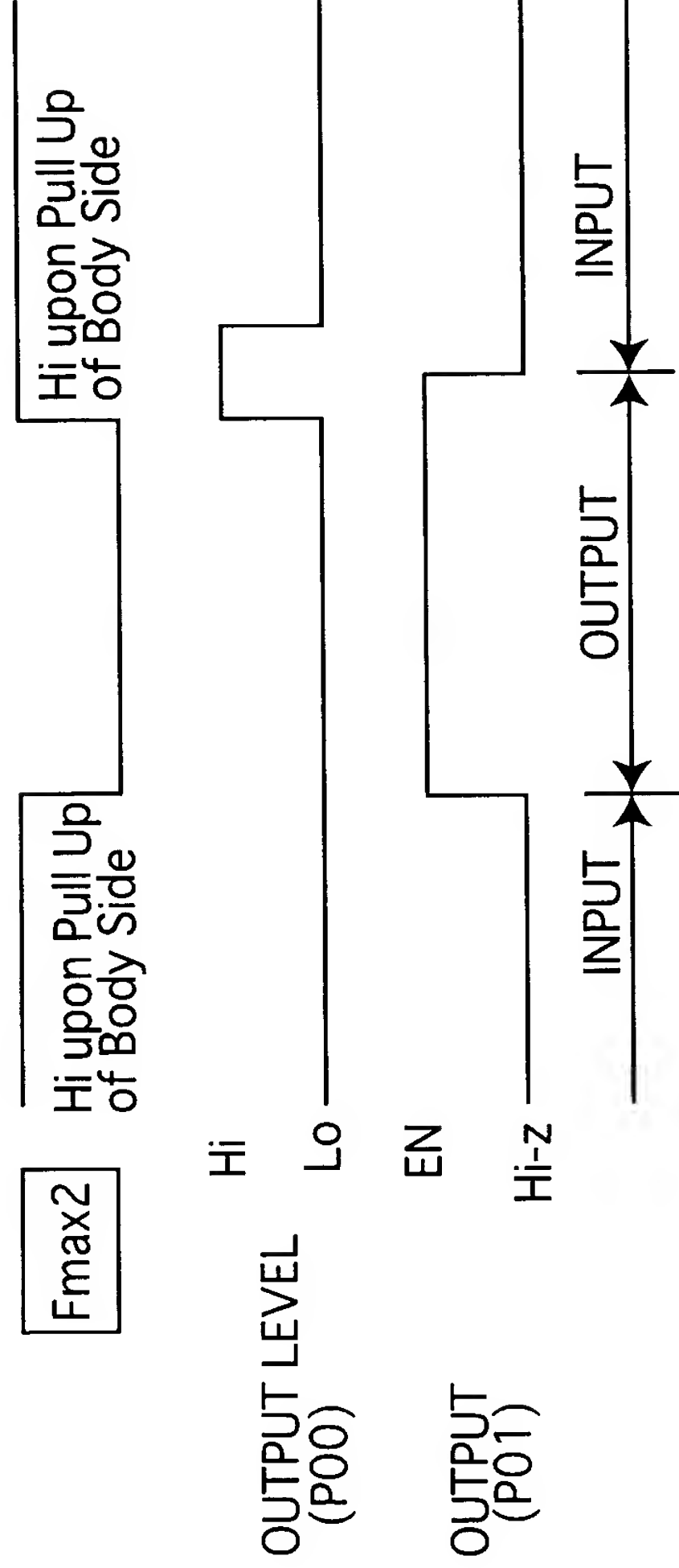


Fig. 20

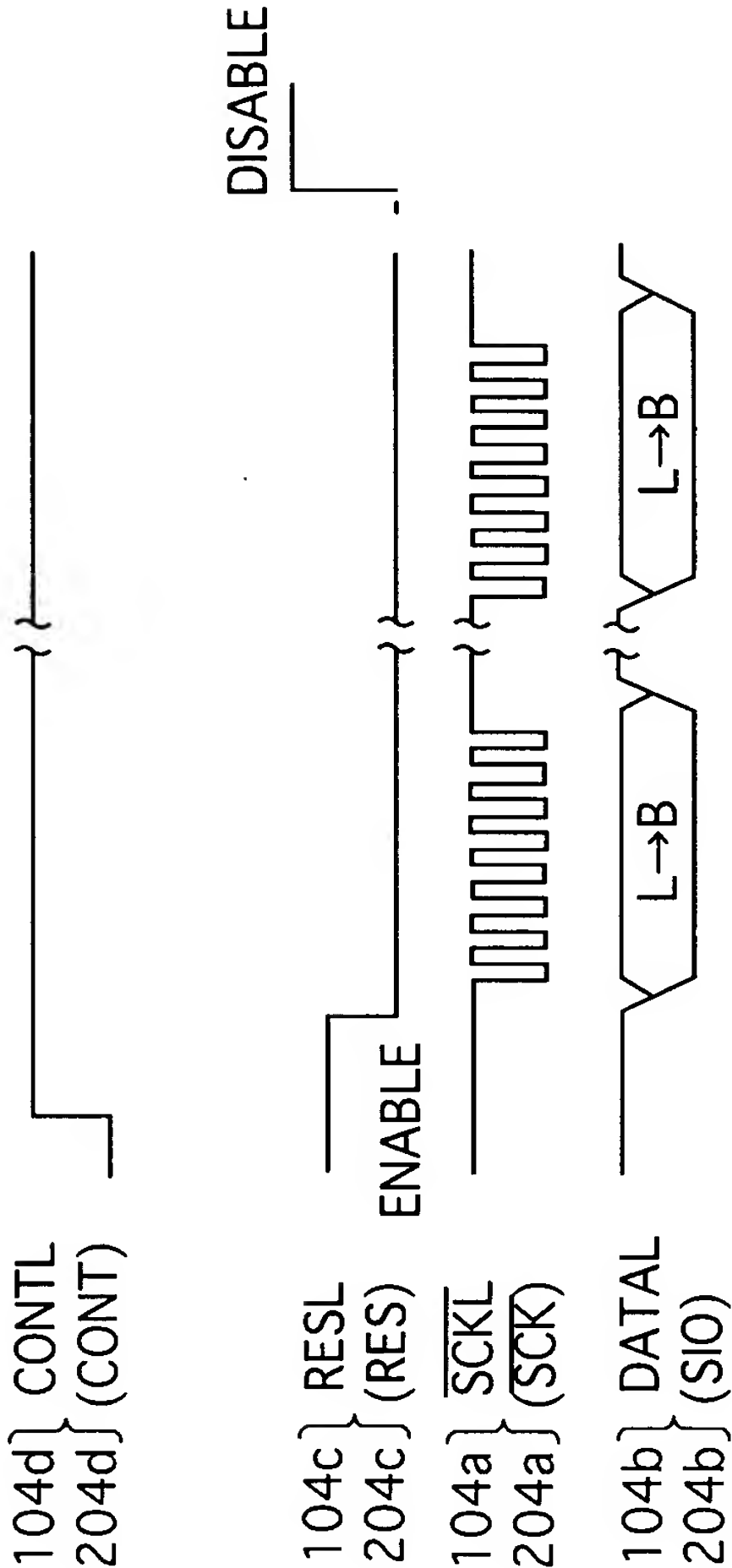


Fig.21A

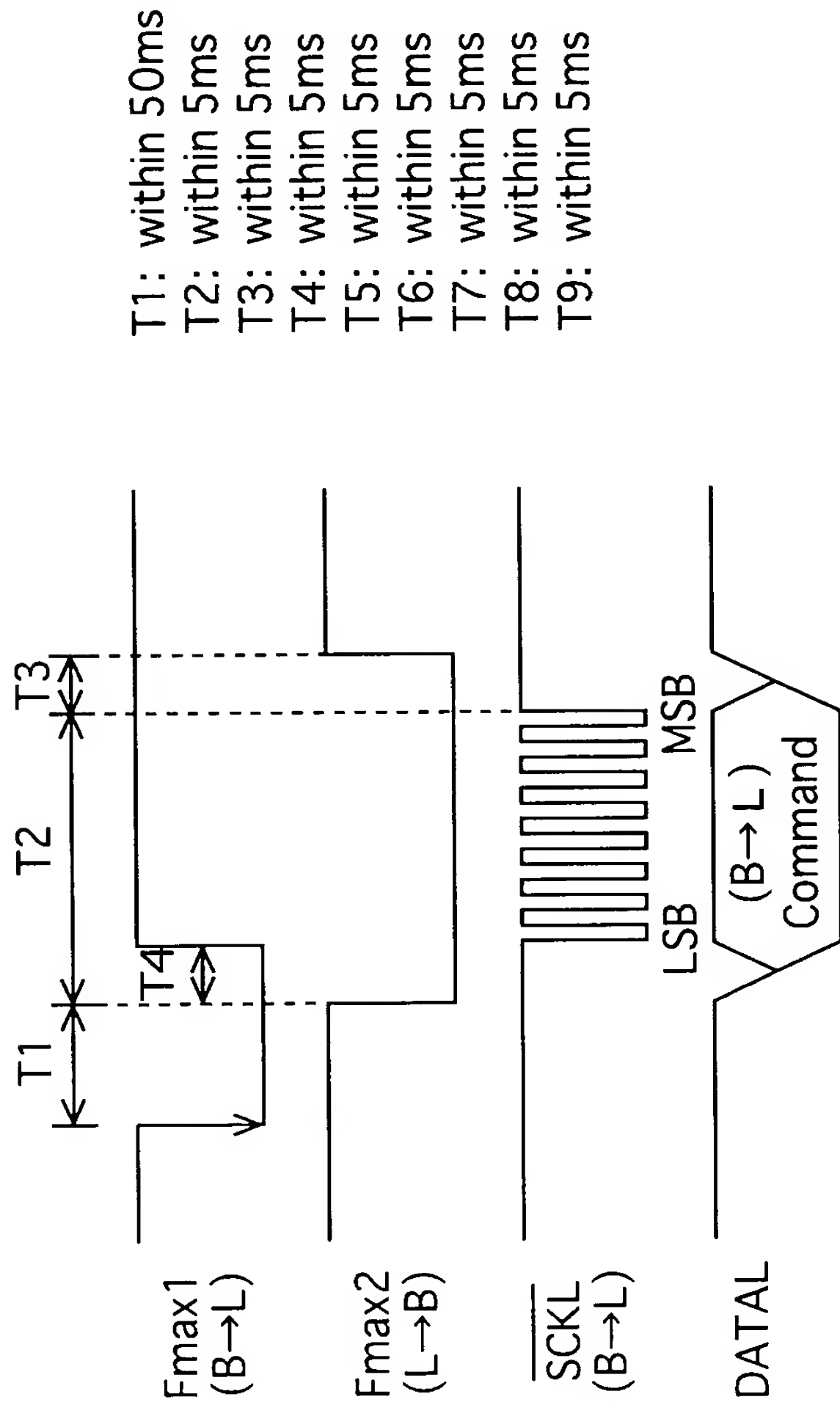


Fig.21B

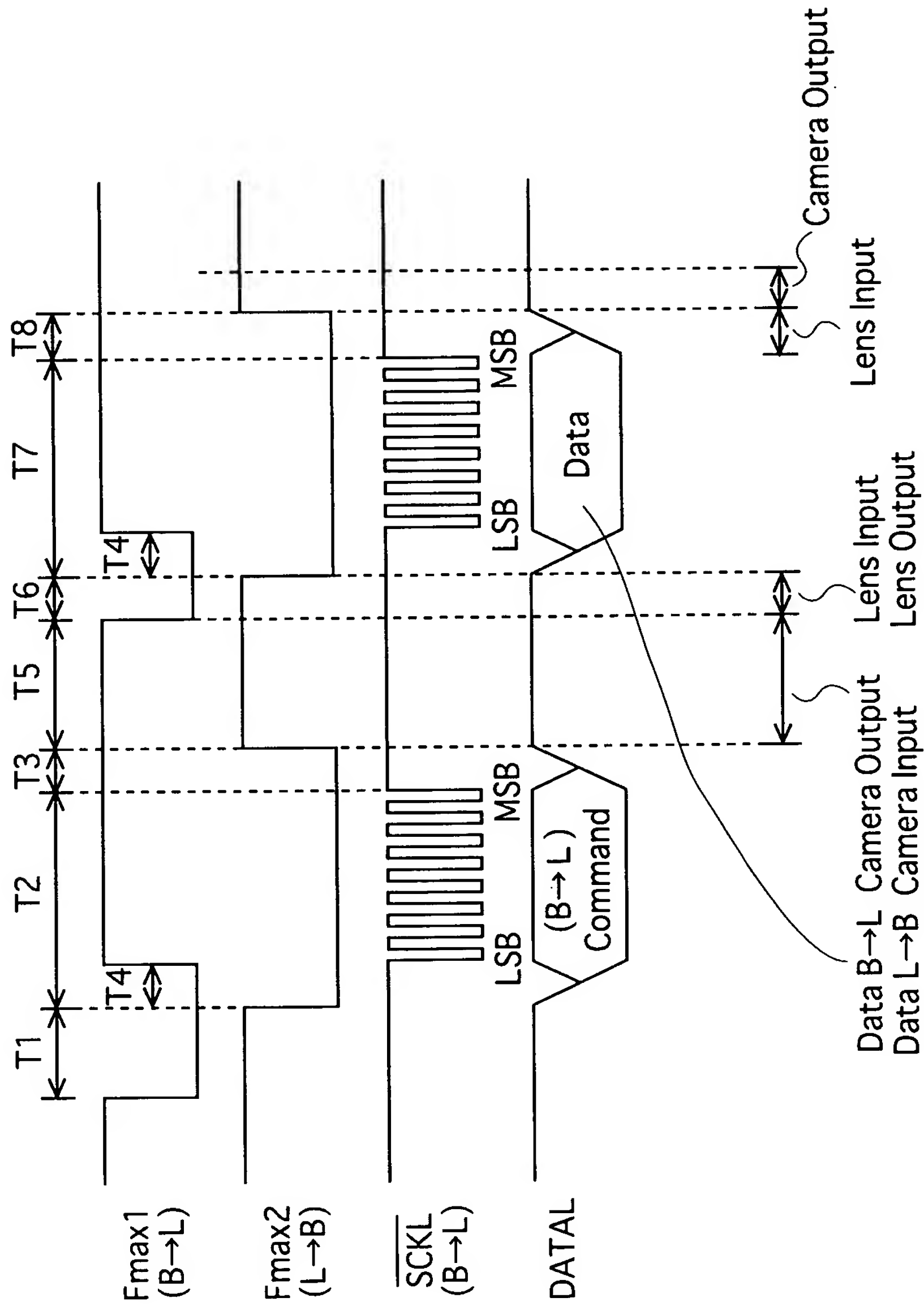
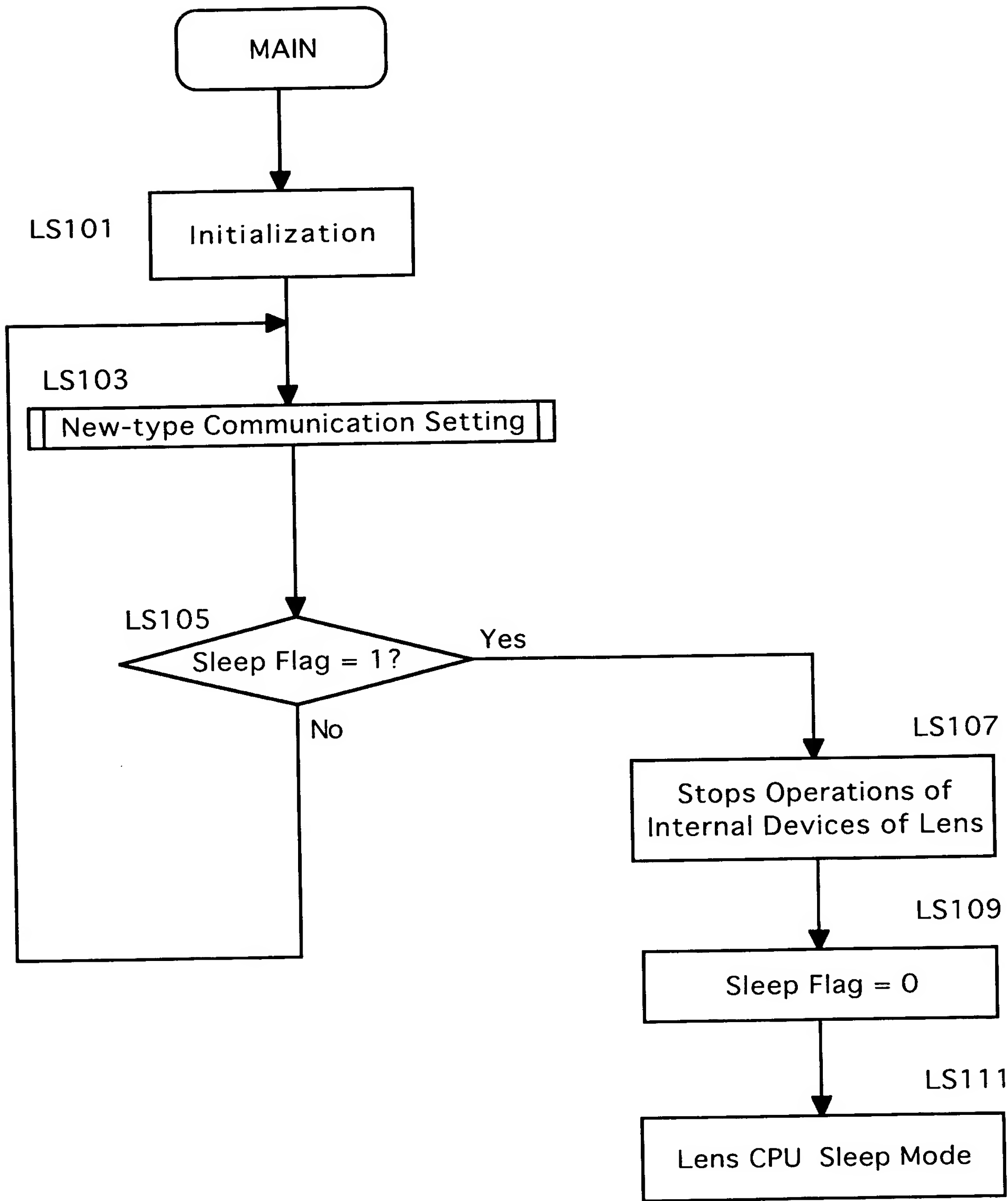
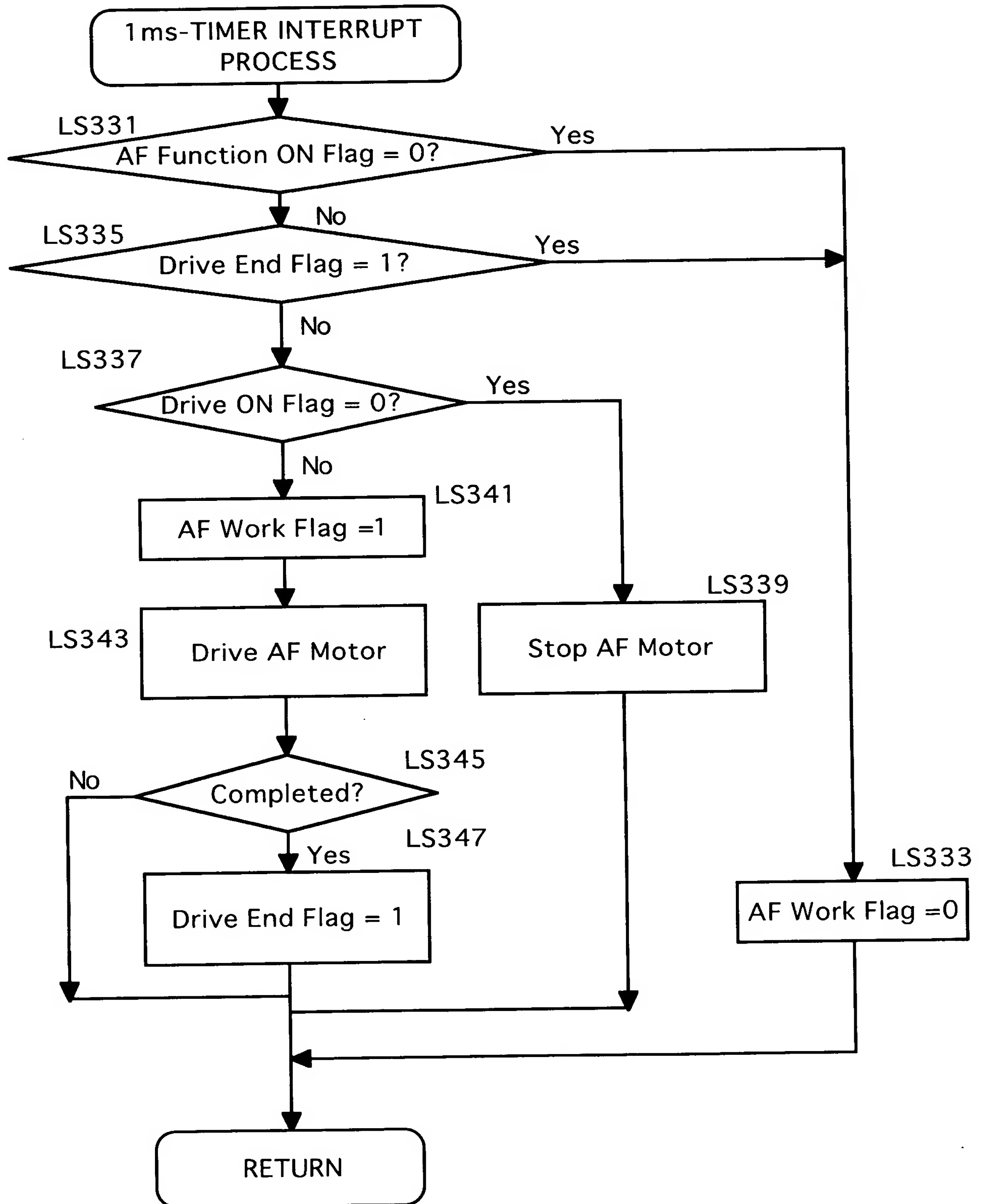


Fig. 23



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Fig. 24



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INVERSE INT INTERRUPT PROCESS

Fig. 25

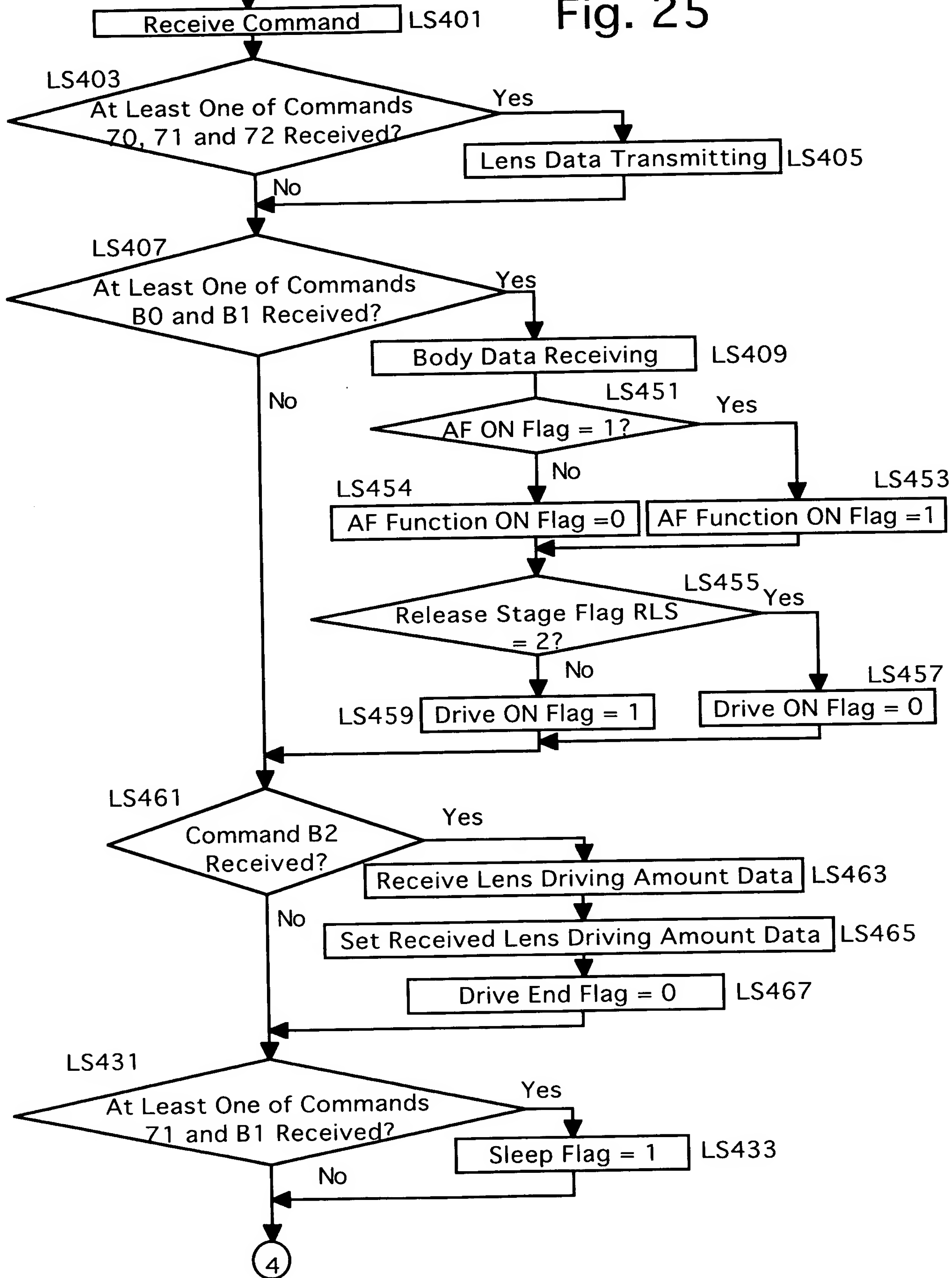
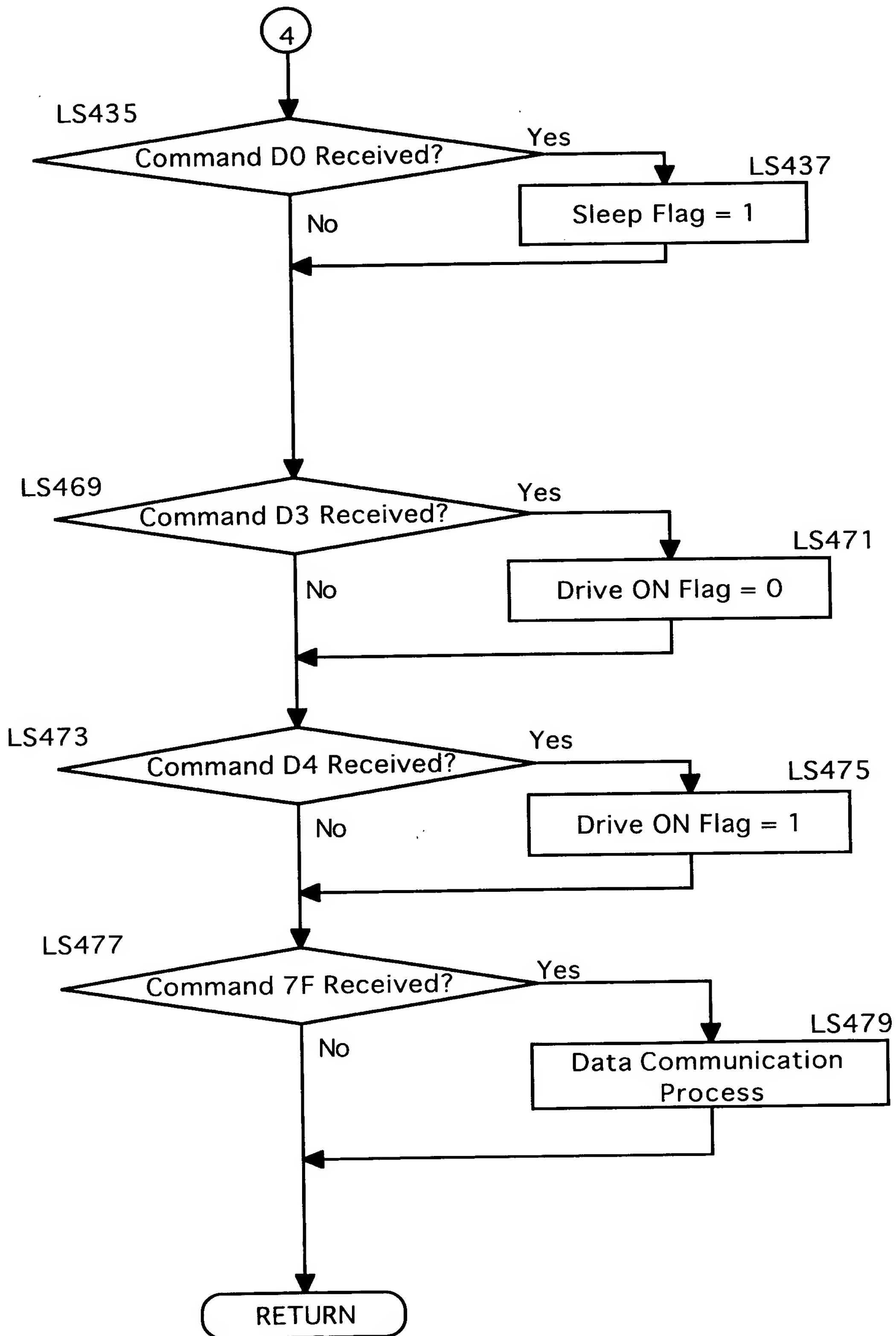


Fig. 26



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Fig.27

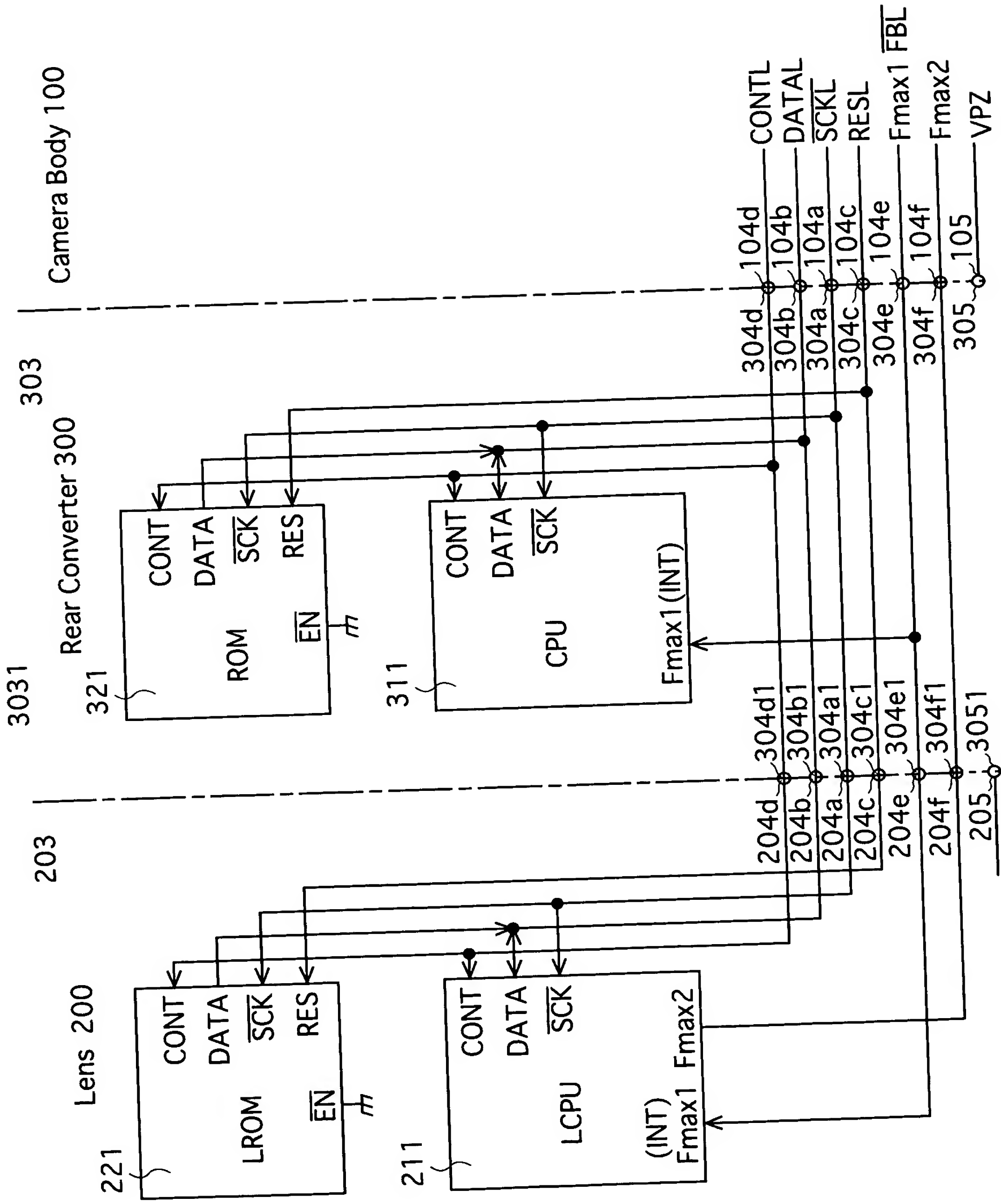


Fig. 28

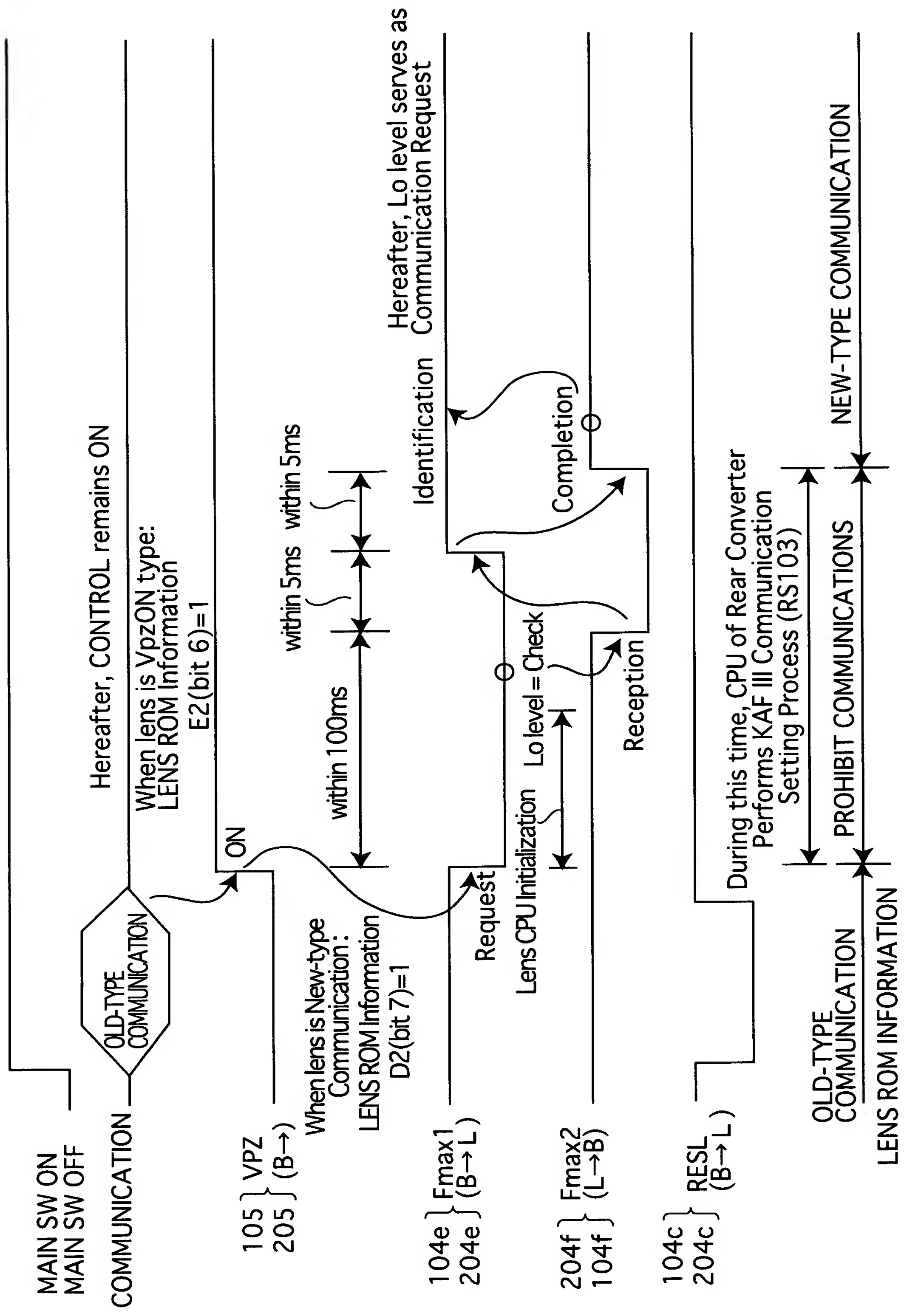
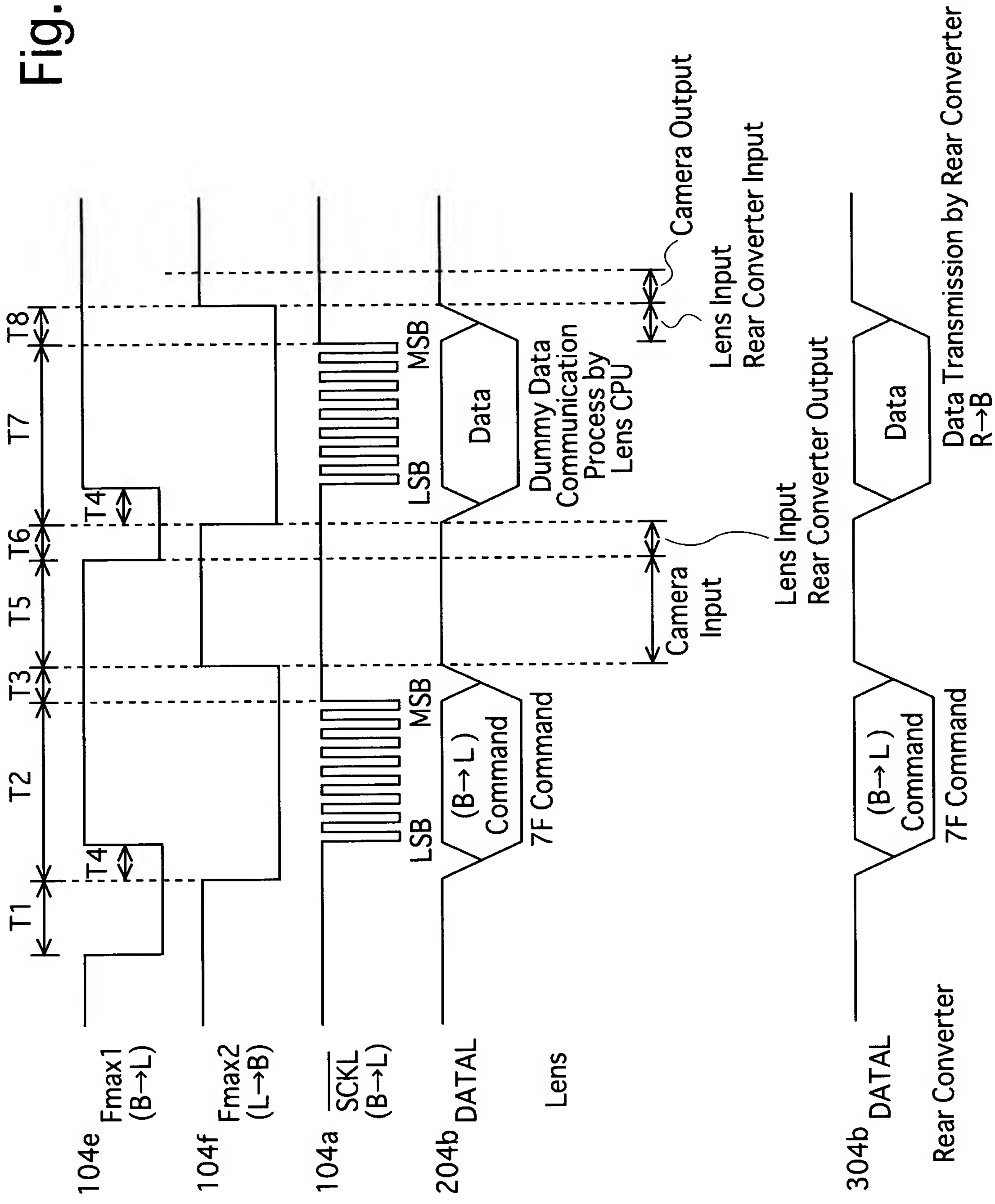


Fig.29



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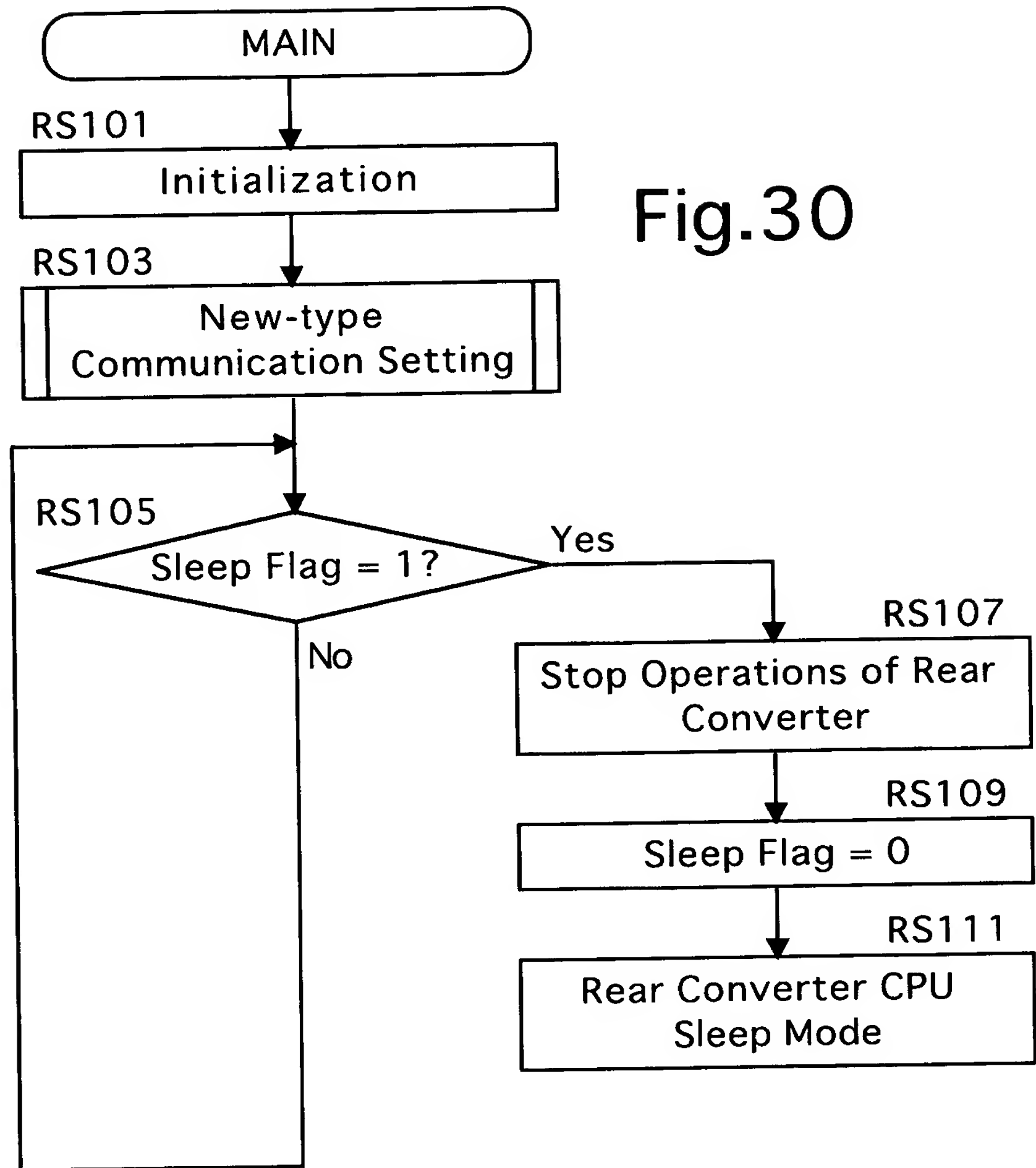
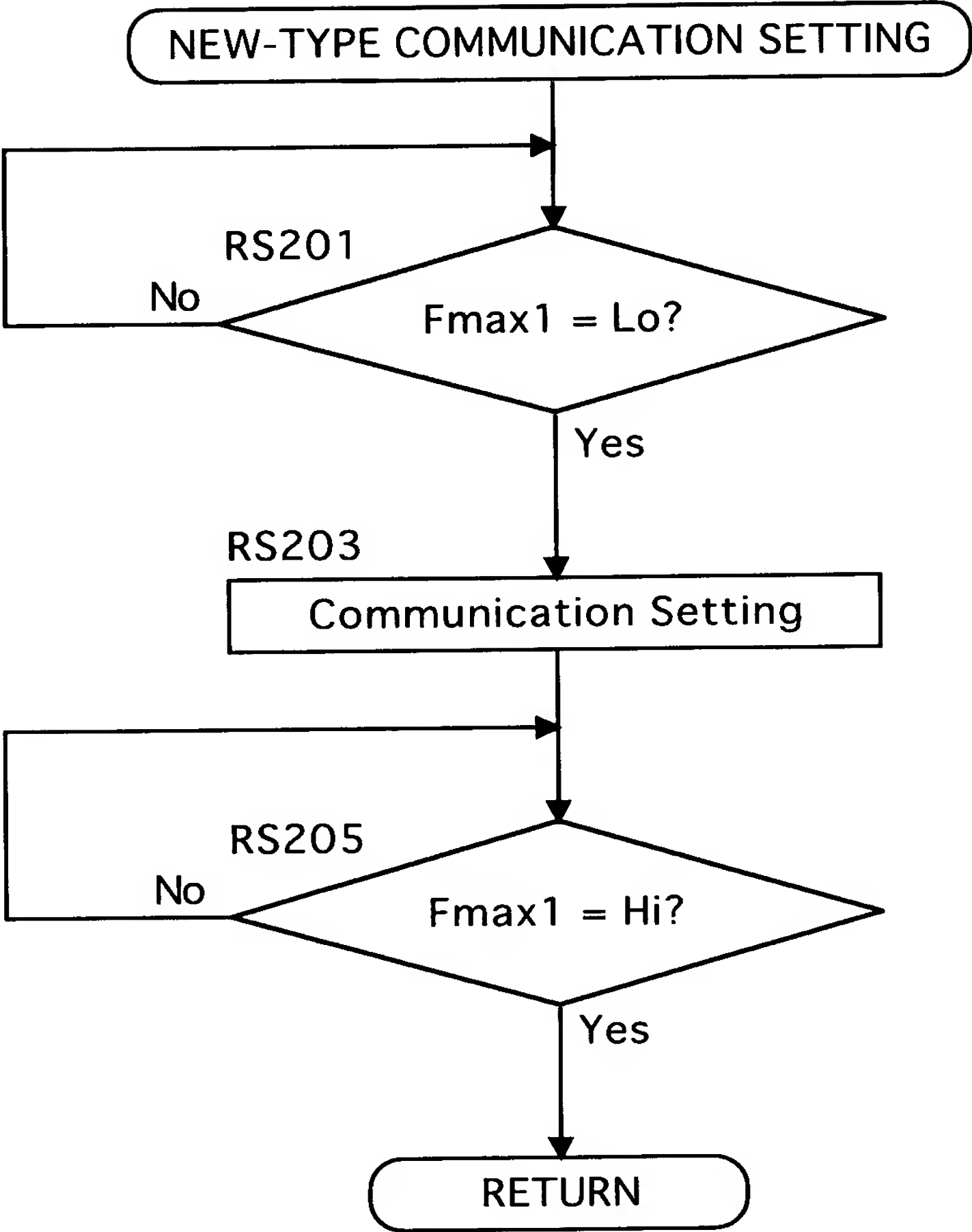
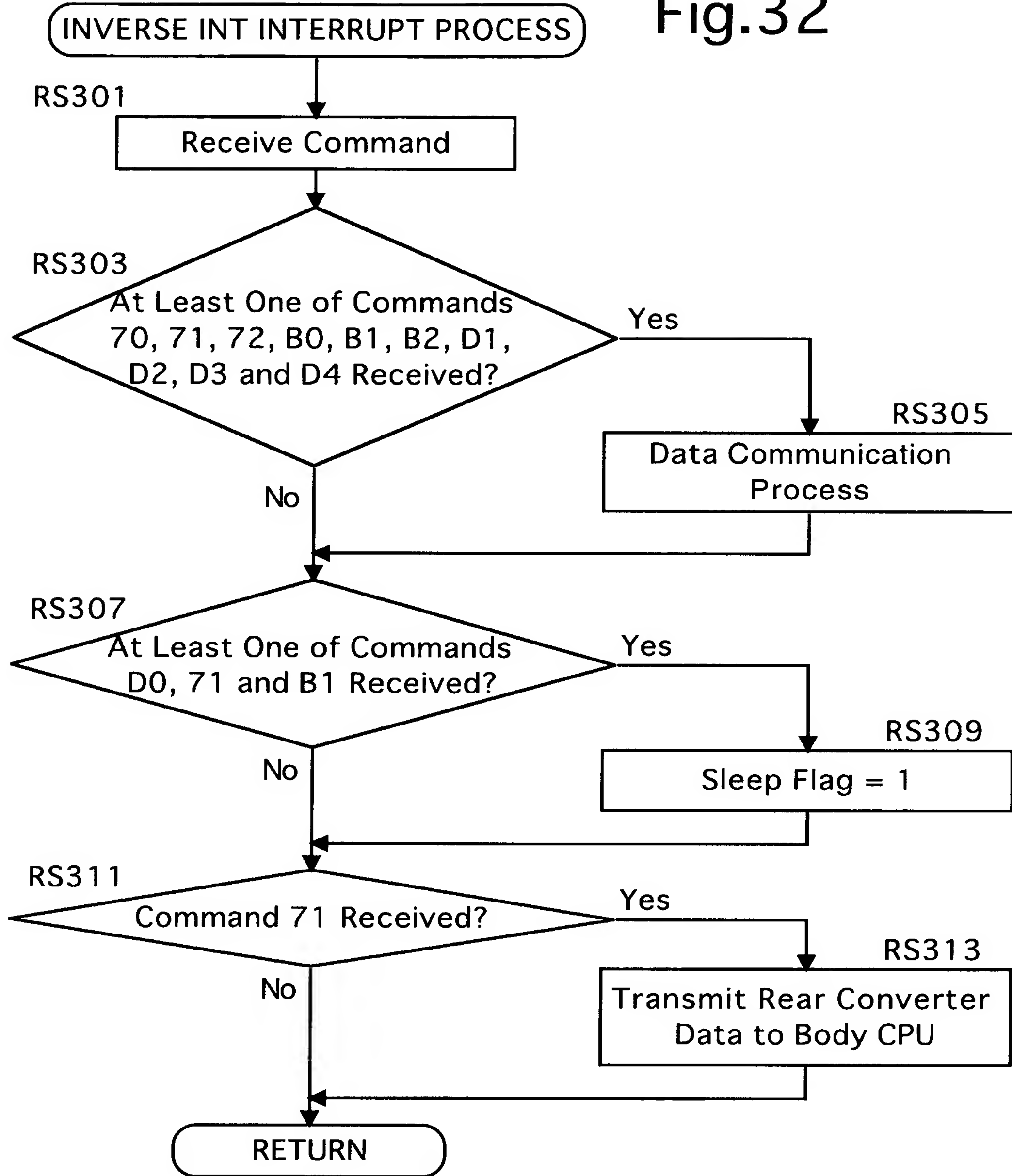


Fig.31



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Fig.32



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